TF5000CI

Service Manual

31 Jan, 2004

Topfield Co., Ltd

IMPORTANT

Note : The design of the satellite receiver is subject to continuous development and improvement. Consequently, this receiver may incorporate minor changes in detail from the information contained in this manual.

Warning : These servicing instructions are for use by qualified personnel only. To reduce the risk of electric shock, do not perform any servicing other than that specified in the operating instructions unless you are fully qualified to do so.

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1. Safety Instructions

Read this chapter carefully before servicing the IRD.

1.1 The IRD must be disconnected from the mains plug before it is opened.

1.2 The capacitor inside the SMPS (power supply) can hold charge even if the IRD has been disconnected from the mains plug. To handle SMPS, wait until the capacitor is discharged.

1.3 Only the same screw should be used to assemble the IRD.

2. List and Description of The Major Parts

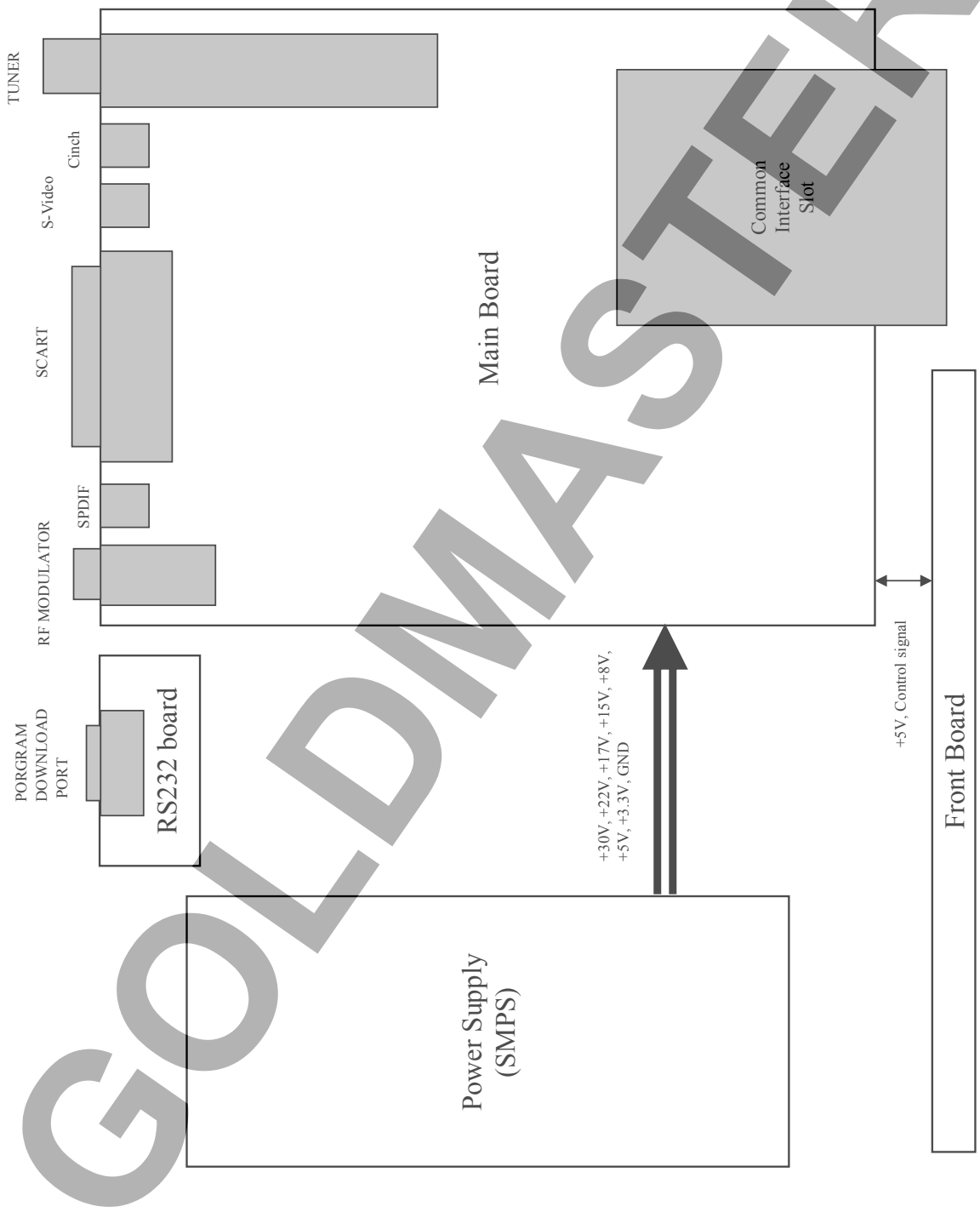
2.1. Main Board

Page	Part Name	Location Number	Part number	Function	Comment
1	Tuner Module	U1	TBMU24311IPP	Channel tuning. Analog to Digital Conversion. QPSK demodulation.	Or, equivalent part
	Regulator	U2	LM7805 (with Heat sink)	Regulates Tuner 5V	Or, equivalent part
	LNB Power Switching IC	U3	LNBP20PD	Regulates and switching LNB power (Horizontal 18V, Vertical 13V) 22KHz tone On/Off LNB Power Bypass	
	Poly Switch	U4	RXE065	Over current protection of LNB power.	
3	CPU, Demux and Decoder	U5	IBM39STB02500	Main CPU of IRD MPEG Demux and Decoder	
4	Flash Memory	U7	SST39VF800A	Saves program and constant	Or, equivalent part
	EEPROM	U8	24LC02B-SN	Saves some parameters	Or, equivalent part
5	SDRAM	U9	K4S641632D	Main system memory	Or, equivalent part
6	ASIC	U10	TF301SC10	CI interface, System control	
	Reset IC	U11	ELM9727NBA	Power level detection, Resets the system.	Only one IC is used
7	Regulator	U12	LD1117ADT18	Regulates internal 1.8V	Or, equivalent part
	Regulator	U13	MIC39100-2.5BS	Regulates internal 2.5V.	
	Regulator	U14	78L12	Regulates internal 12V.	
	Connector	JP2	5267-12A	Power input connector from SMPS	
8	RS232 Driver	U15	MAX232	Rs232 level conversion	Or, equivalent part
	Connector	JP3	5267-3A	Connector for RS232 sub board	
9, 10	TTL	U17, …	74 series	Buffer and mux for CI	Or, equivalent part
	FET	U16	IRF7303	CI Power On/Off	Or, equivalent part
	Connector	U20	PIS2B1382	PCMCIA connector for CI	
11	Audio DAC	U31	UDA1334TS	Audio Digital to Analog Converter	
12	A/V Switch	U33	STV6412A	A/V switch for SCART and Cinch	
	SCART	SCART1	2203-42STA	SCART connector	
	Cinch	J1	RCA – 3pin	Cinch connector for A/V	
13	Regulator	U34	LM7805 (without Heat sink)	Regulates 5V for RF-modulator	Or, equivalent part
	RF Modulator	U35	RMUP74055AB	RF Modulator	
15	Connector	JP9	5267-7A	Front Board Interface	
	TTL	U37	74LVC14	Front Board Interface	Or, equivalent part
16	Connector	JP10	mini Din Jack	S-Video Connector	
17	Connector	JP7	5267-10A	Smart Card Sub board connector	Embedded CAS, Optional

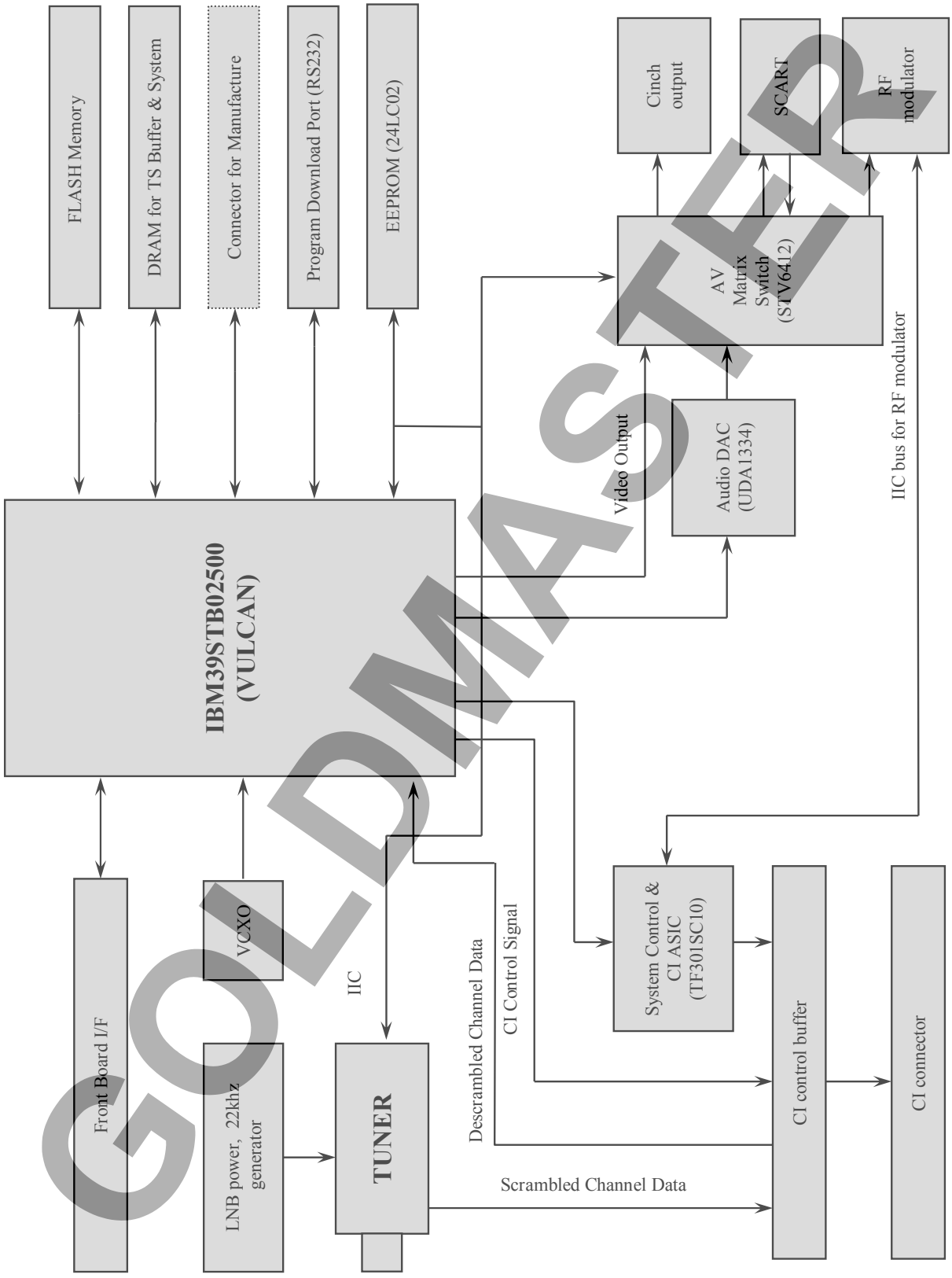
2.2. Front Board

page	Part Name	Location Number	Part number	Function	Comment
1	7-Segment	U1	A-3C4G	Displays Messages	Or, equivalent part
	Remocon Sensor	U2	TSOP4838	Receives RCU signal	Or, equivalent part
	TTL	U3, U4	74HCT164	Interface front board with main board	Or, equivalent part
	Connector	J1	5267-7A	Front board and main board interface	

3. Block Diagram of The IRD



4. Block Diagram of The Main Board



5. Test and Repair

5.1. Visual Test

- Check whether all the connectors are plugged well.
  'JP2' of Main Board : Power connector.
  'JP3' of Main Board : Internal RS232 connector.
  'JP5' of Main Board : SPDIF connector.
  'JP7' of Main Board : Connector for SMART Card sub-Board interface (optional)
  'JP9' of Main Board : Connector for Front Board interface
- Check whether the SMPS(power supply) has any damage.
- Check whether the Main Board has any damage.
- Check whether the Front Board has any damage.
- Check whether the RS232 Sub board has any damage.

5.2. Basic Function Test

5.2.1. No LED and 7-segment is turned on

	Possible Cause	How to Check	How to repair
1	Front Board Problem	Replace the Front Board with new one which works well in other IRD	If it works, repair the Front Board. Otherwise, check the Main Board and SMPS.

5.2.2. Some LED and 7-segment have problems

	Possible Cause	How to Check	How to repair
1	Front Board Problem		Check the Front Board according to the advanced function test.

5.2.3. Remote Control Unit (RCU) does not work.

	Possible Cause	How to Check	How to repair
1	Remote Controller may have some problem.	If some keys of RCU do not work, it may be RCU problem.	Replace the RCU with new one.
2	Sensor of the Front Board may have problem	If key, LED and 7-segment work, And only the Remote control does not work, it is sensor problem.	Check the PCB pattern of Front Board. Check the power of U2(sensor). Replace the sensor.

5.2.4. Key of the front panel have problems

	Possible Cause	How to Check	How to repair
1	If some of the key does not work, it is the Front Board problem. Pattern or broken tact switch can be a problem	If one of the key or RCU work, it is the problem of the switch or PCB pattern of the Front Board.	Check the switch and the PCB patterns of Front Board.
2	If sometime RCU work, but the key of the front panel does not work, then one of the key may be pressed always.	Check J1.3 in Front Board or Jp9.3 in main board. This pin should be “HIGH” when no key is pressed.	Replace the tact switch. Check the PCB and remove the short to the GND. Check the Front board resistors

5.2.5. No System ID is displayed

	Possible Cause	How to Check	How to repair
1	Main Board problem. Communication problem between the Front Board and the Main Board.	Replace the Front Board with new one which works well in the other IRD	If it works, repair the Front Board. If it does not work, repair the Main Board.
2	Main Board fails to boot.	The 7-segment on the Front Board displays only the time with brighter display when the power key is pressed.	Repair the Main Board. Check the powers of Main Board.

REF) System ID : When the power is turned on, 7-segment on the Front panel displays it.
Ex) 'L5.01' is displayed : its System ID is 501

5.2.6. Receiver acts like the key of the Front Board or RCU is pressed.

	Possible Cause	How to Check	How to repair
1	The key of the Front Board is pressed always.	Replace the Front Board and check it. Check J1.3 in Front Board or Jp9.3 in main board. This pin should be "HIGH" when no key is pressed.	Replace the broken key with new one. Check the Front PCB. Replace the tact switch. Check the PCB and remove the short to the GND. Check the Front board resistors

5.2.7. No picture but the OSD works.

	Possible Cause	How to Check	How to repair
1	Tuner problem	If the signal level of the tuner is very low, it may be a problem of the tuner, antenna cable or antenna.	Check the antenna signal. Check the tuner part.
2	No or bad LNB power No or bad 22khz signal.	Check the LNB power and 22khz signal on LNB in of the tuner.	See LNB section of this manual.
3	The power of the tuner has some problem.	If the signal level of the tuner is very low, check the voltage of the U1.7 (tuner). It should be about 30V.	If not, check the SMPS and the power path (include series bead(L5) and capacitors (C9, C7)
4	CPU (IBM39STB02500) problem	If all the other things work except the picture and sound, it may be the problem MPEG decoding. In this case, the signal level and signal quality of the information bar will be good.	
5	Channel Data path problem. (include CI interface Circuit)	There is good RF signal level, and good signal quality, but no broadcasting is scanned. In this case, it may be a channel data path problem.	Check the channel data path of the Main Board.

5.2.8. No picture (and no OSD) and No sound

	Possible Cause	How to Check	How to repair
1	CPU (IBM39STB02500) problem.	In this case, the OSD have some problems.	Check SMPS and Main Part of main board voltage
2	A/V switch problem	If the front works well and nothing is appear on TV, it can be an A/V switch problem.	Repair the Main Board according to the advanced function test.
3	SMPS problem.	Check the all the power of power connector on Main Board.	Repair the power according to the advanced function test .

5.2.9. No sound and good picture

	Possible Cause	How to Check	How to repair
1	Audio DAC problem	Test the Main Board according to the advanced function test.	Repair the Main Board according to the advanced function test.
2	A/V switch problem	Test the Main Board according to the advanced function test.	Repair the Main Board according to the advanced function test.

5.2.10. No picture(and no OSD) and good sound

	Possible Cause	How to Check	How to repair
1	CPU (IBM39STB02500) problem	Test the Main Board according to the advanced function test.	
2	A/V switch problem	Test the Main Board according to the advanced function test.	Repair the Main Board according to the advanced function test.

5.2.11. No sound and / or no picture on the RF modulator (Cinch works well)

	Possible Cause	How to Check	How to repair
1	RF channel is selected incorrectly.	Check the RF channel selection.	Select the correct channel.
2	RF modulator has problem.	Replace the RF modulator with new one. If it works well, it is the problem of RF modulator.	Replace it with new one.
3.	Problem of Audio or Video line on the board.	Replace the RF modulator with new one. It will have the same problem.	Repair the Main Board.

5.2.12. No LNB power at all of the vertical and horizontal.

	Possible Cause	How to Check	How to repair
1	'LNB power OFF' is selected in the menu.	Check the LNB menu.	Set the LNB power to ON.
2	U3(LNBP20PD)or related circuit has problem.	Test the Main Board according to the advanced function test.	Repair the Main Board according to the advanced function test.
4	SMPS has problem.	Check the SMPS	Replace the SMPS.

5.2.13. Incorrect LNB power

	Possible Cause	How to Check	How to repair
1	If only the 18V is very low, it can be a SMPS problem.	Check the SMPS(or JP2.2). It should be higher than 20V.	If not, replace SMPS.
2	Both the 18V and 13V are too low or too high.	Test the Main Board according to the advanced function test.	

5.3. The Advanced Test of Main Board.

5.3.1. Voltages on important point

- Voltage at JP2 in page 7 (of a schematic diagram)

Pin number	Minimum voltage	Nominal voltage	Maximum voltage	Comment
1	+28V	+30V	+32V	
2	+20V	+22V	+23V	
3	+16V	+17V	+18V	
5	+7.6V	+8V	+8.4V	
7	+4.75V	+5V	+5.25V	
9	+3.22V	+3.3V	+3.38V	In standby mode, it can be higher than maximum voltage
11	+14V	+15V	+17V	
4,6,8,10,12	GND	GND	GND	

- Voltage at check points in standby mode.

check points	page	Nominal voltage	Comment
JP9.1	15	+5V	Power is supplied to the Front Board even if standby mode
U14.3	7	+12V	Power is supplied to SCART Circuit even if standby mode
C50	12	+5V	Power is supplied to SCART Circuit even if standby mode

- Voltage at check points in normal mode.

check points	page	Nominal voltage	Comment
JP9.1	15	+5V	Power is supplied to the Front Board even if standby mode
U14.3	7	+12V	Power is supplied to SCART Circuit even if standby mode
C50	12	+5V	Power is supplied to SCART Circuit even if standby mode

* **important** : Be careful not to short the signals while checking the signals. It may damage the other part of Main Board.

5.3.2. Power on Test sequence

- Check point 1 -

JP9.1 is the power supply pin of the Front Board. It should be 5V.

If it is not 5V, remove the Front Board from the JP9. And, check JP9.1 again.

If it is 5V, the Front Board have problem. Otherwise, the Main Board or SMPS have problem.

- Check point 2 –

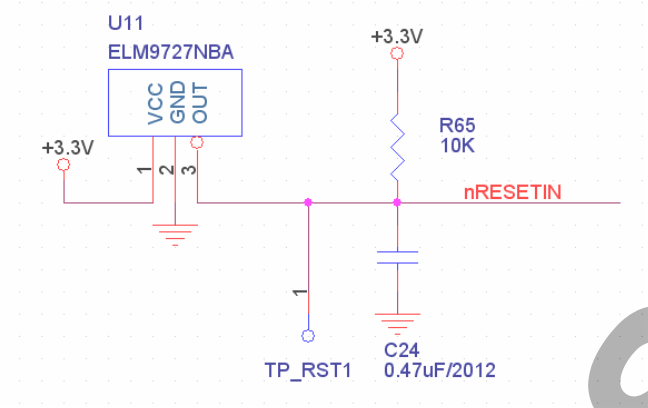
Replace the Front Board with new one. If it does not work, it is the problem of Main Board. Check the voltages of some

points according to **'Voltages on important point'** section.

5.3.3. Reset

After power on by the Front Board, reset circuit works.

Schematic page 6.



U11 is a voltage detector. If the voltage of 3.3V is lower than 2.7V its output goes to low.
The 'nRESETIN' signal should go high after power up (when the IRD goes to Normal state from Standby state.)
The reset signal is delayed and reconstructed in U10(TF301SC10). The reset output of U10.51 is provided to all the system.

- Check point 1 -

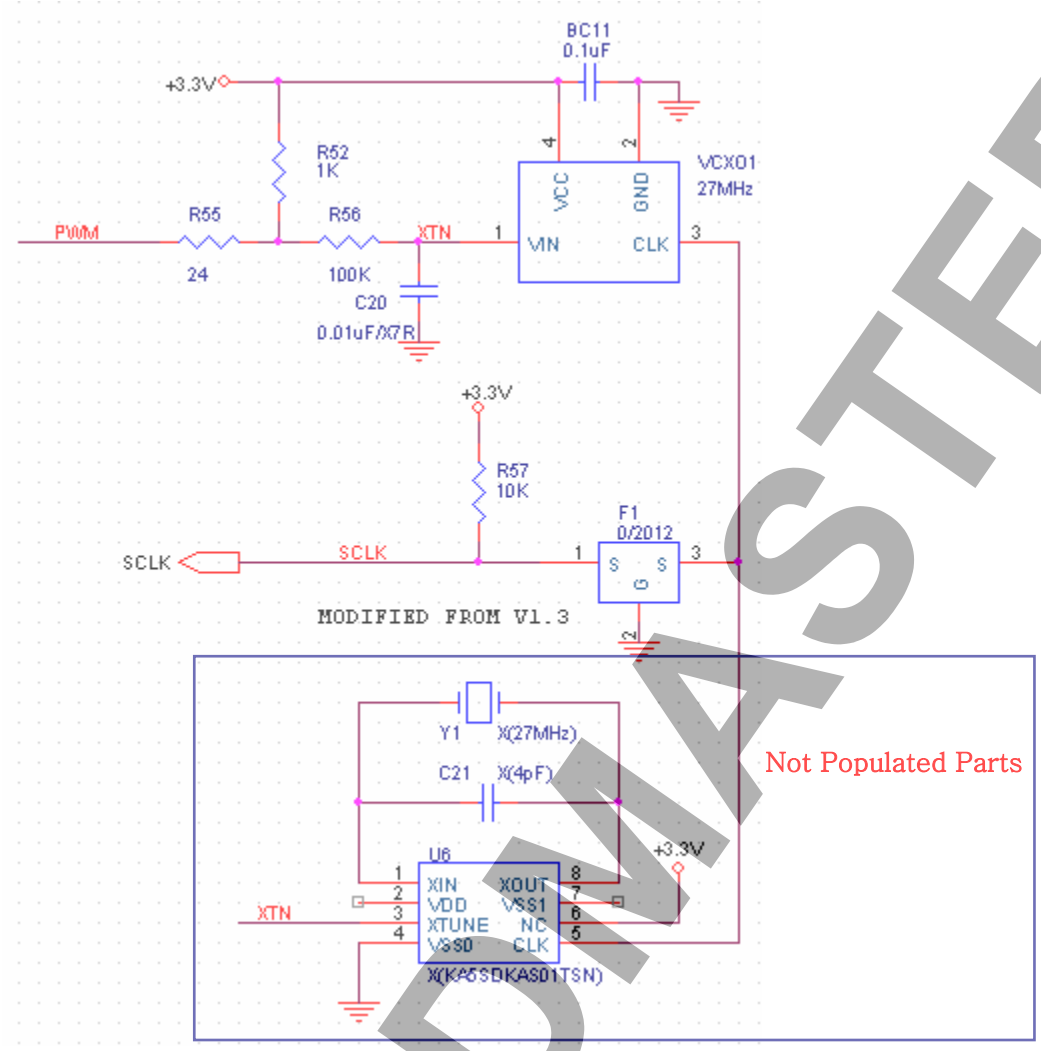
In normal state, the 'nRESETIN' signal is 3.3V. If it is about 0V, U11 has problem.

- Check point 2 -

U10.50 and U10.51 should be 3.3V. If only U10.50 is 3.3V, check the system clock.

5.3.4. System Clock

Schematic page 3



VCXO1 generates the system clock.
The 'SCLK' signal is 27.000MHz clock signal.
If the color of the picture disappears, the 'SCLK' signal may be different from 27.000MHz or the VCXO1 have bad quality.
In this case, replace VCXO1 with new one.

- Check point 1 -
  Check the input and output of 'F1'. All of them should have 27MHz clock signal.
  If F1.3 have not 27MHz clock signal, it may the problem of VCXO1.

- Check point 2 -
  If the video output of receiver has not color, It may the problem of VCXO1.

5.3.5. RS232 Data Port (Program download port) and Program download

Connect a PC with a download cable (Female – Female cross cable). If it fails program download and nothing happens in the receiver, check the download cable and the PC. The pin2 and pin3 of the download cable should be crossed.

- Check point 1 -

Check the download cable and the PC with a new receiver.

- Check point 2 -

Check the error code on the display of the Front Board. Some message is displayed on the Front panel when the new program is downloaded. The message and error code is as follows.

Display	Description
dn##	Data is being downloaded. ('##': the number of remained data block)
LP##	Loader program is being saved. ('##' : the number of remained flash block to write the loader program.)
AP##	Application program is being saved. ('##' : the number of remained flash block to write the application program.)
Fd##	Flash data program is being saved. ('##' : the number of remained flash block to write the flash data.)
Ed##	EEPROM data is being saved. ('##' : the number of remained EEPROM block to write the EEPROM data.)
E-01	The CRC error of Header/Data block.
E-02	The CRC error of Application program.
E-03	UART communication error.
E-04	Error while Flash writing.
E-05	Memory overflow
E-06	Different system ID. -> The model of the receiver and the program(or data) to be downloaded is not matched.
E-07	Not supported TFD version.
E-08	Not supported data type.
E-09	EEPROM read error.
E-10	EEPROM write error.
E-11	Not supported Flash memory.
E-12	Error while TFDM writing.

5.3.6. LNB power

U3.5 is the control signal for selecting vertical or horizontal. At vertical, level of this pin is logical "LOW", about 0. And at horizontal, logical "HIGH", about 3V. U3.4 is the output voltage for tuner (LNB). At vertical, Voltage at this pin is about 13V and at horizontal, about 18V.

- Check Point 1 -

If the voltage of U3.2 has below 15V, check the voltage of SMPS according to ' Voltages on important point' section.

If the voltage of U3.3 has below 20V, check the voltage of SMPS according to ' Voltages on important point' section.

Set the LNB voltage to Vertical.

- Check Point –

Check level of U3.5 whether logically 'LOW' (about 0V). If logically 'HIGH' (over 2V) then check the line from U5 (IBM39STB02500) and U3.5.

Check level of U3.6 whether logically 'HIGH' (about 3V). If logically 'LOW' (about 0V) then check the line from U5 (IBM39STB02500) and U3.6.

Check the voltage of U3.4. That value must be about 13V. Voltage of U3.4 is under 12V, then check soldering status of U3.

Set the LNB voltage to Horizontal

- Check Point –

Check level of U3.5 whether logically 'HIGH' (about 3V). If logically 'LOW' (about 0V) then check the line from U5 (IBM39STB02500) and U3.5.

Check level of U3.6 whether logically 'HIGH' (about 3V). If logically 'LOW' (about 0V) then check the line from U5 (IBM39STB02500) and U3.6.

Check the voltage of U3.4. That value must be over 20V. Voltage of U3.4 is under 20V, then check soldering status of U3.

5.3.7. RF modulator

- Check point 1 -

Check the power input of RF modulator (U35, schematic page 13)

- Check point 2 -

Check the IIC line.(the signal name is SDA5V, SCL5V).

5.3.8. Video

For CVBS

- Check point 1 -

Check L16 and its related circuit (CVBS), L17 and its related circuit (RED), L19 and its related circuit (GREEN) and L21 and its related circuit (BLUE). At this point all the signal should be work. If it works well, U33 or its related circuit has problem. If it does not work well, the IBM39STB02500 (U5) may have problem.

For S-VIDEO

- Check point 1 -

Check JP5 S-Video connector and its related circuit.

5.3.9. Audio

- Check point 1 -

Check U31(UDA1334TS) and its related circuits from schematic page 11. And check C60, C63 and U33 from schematic page 12. All these this has no problem, then check SCART Connector (SCART1) and Cinch Jack(J1).

5.3.10. SCART bypass

- Check point 1 -

If SCART bypass has problem, check U33 and its related circuit.

5.4. The Advanced Test of Front Board.

* Applicable Front Board : TFCB-7KEY, SIRIUS-5KEY.

5.4.1. Key

If any key pressed, J1.3 in Front board goes low and high repeatedly. If level of this pin never goes to “LOW”, main board problem,

5.4.2. Remote control

U5 is the sensor for remote control.

- Check point 1 –

Check the Remote control unit with other receiver. If it does not work, the Remote control unit may have problem.

If the key and display work and only the remote control does not work, U5 (sensor) may have problem.

Check the power of the sensor (U5.3). It should 5V. If not, check the R10 and the PCB.

- Check point 2 –

If you can not find any problem with the U5.3, replace it with new one.

5.4.3. Display

- Check point 1 –

If one of the digit is brighter than the other, and the digit displays wrong character, then check resistors and transistors in front board. If there is no wrong value or parts, then replace transistor with new one.

- Check point 2 –

If one of the digit is not displayed, replace the transistor Q1 to Q5 as follows.

First Digit -> Q1
2nd Digit -> Q2
3rd Digit -> Q3
4th Digit -> Q4
dots and LED on the display -> Q5

- Check point 2 –

If one of the segments in one digit is not displayed, replace the display module.

- Check point 3 –

If one of the segments of all digit is not displayed, check the resistors (R1, R2, R3, R4, R5, R7, R8, R9) and the PCB pattern.

5.4.4. Nothing works on Front Board.

- Check point 1 –

The power of Front board (J1.1), U2.3, U3.14, U4.14

5.5. The Advanced Test of SMPS.

* There are two kinds of SMPS. : ORTP-826, HDAD30W701.*

---------- **Caution** ----------

I. The SMPS must be disconnected from the mains plug to test.
II. The capacitor inside the SMPS (power supply) can hold charge even if the IRD has been disconnected from the mains plug. To handle SMPS, wait until the capacitor is discharged.
III. Very high voltage is generated in SMPS.

5.5.1. Check the damaged parts.

- Check point 1 –

Check whether there is broken part by visual test.

5.5.2. Test the diodes.

Check point 1 –

Check whether the diodes has crack. If there is a crack, replace it with new one.

Same part should be replaced.

- Check point 2 –

Check the resistance of all the diodes. If the resistance is too low (lower than 10ohm), it is the problem.

Replace it with new one. Same part should be replaced. Diodes is named as Dxx or ZDxx.(ex: D1, ZD1)

To measure the resistance, you must remove the mains plug.

5.5.3. Check the 3.3V regulator

- Check point 1 –

If only the 3.3V has problem, check the U3.VI. If it is about 4V, check the shunt regulator.

5.5.4. Check the Shunt regulator

- Check point 1 –

Check the voltage of U2 -cathode. It should higher than 3V. If not, remove U2 and check it again.

If it is higher than 3V, replace U2 with new one.

- Check point 2 –

If U2 -cathode is higher than 3V, the voltage at U2 -ref should 2.5V. If not, replace U2 with new one.

5.5.5. Check the Photo coupler IC

- Check point 1 –

Check the photo coupler PC1. Check the resistance of diode part and photo Transistor part.

If its resistance is about 0 or very low, replace it with new one.

Check if it has damages. And replace it with new one.

5.5.6. Check the Fuse.

- Check point 1 –

Check the fuse. Before replacing the fuse, check the other problem which can exist.

The same kind and rated fuse should be replaced..

6. PIN description of The Major Parts

6.1. Main Board

Tuner Module	U1	TBMU24311IPP
Regulator	U2	LM7805
LNB Power Switching IC	U3	LNBP20PD
CPU, Demux and Decoder	U5	IBM39STB02500
Flash Memory	U7	SST39VF800A
EEPROM	U8	24LC02B-SN
SDRAM	U9	K4S641632D
Reset IC	U11	ELM9727NBA
Regulator	U12	LD1117ADT18
Regulator	U13	MIC39100-2.5BS
Regulator	U14	78L12
RS232 Driver	U15	MAX232
FET	U16	IRF7303
Audio DAC	U31	UDA1334TS
AV Switch	U33	STV6412
RF Modulator	U35	RMUP74055AB

- See the Pin description Section -- A. 1

6.2. Front Board

Part Name	Location Number	Part number
Remocon Sensor	U2	TSOP4838
TTL	U3, U4	74HCT164

- See the Pin description Section -- A. 1

6.3. SMPS : ORTP-826 (HDAD30W701)

Part Name	Location Number	Part number
Shunt regulator	U2	KA431A (AZ431BZ-B)
SPS	U1	KA1M0380R (KA5M0365R)
3.3V regulator	U3	KA278R33

- See the Pin description Section -- A. 1

7. Schematic Diagrams

7.1. Schematic diagram of Front Board

- See the Schematic Diagram Section < Front Board > -- A. 2

7.2. Schematic diagram of Main Board

- See the Schematic Diagram Section < Main Board > -- A. 3

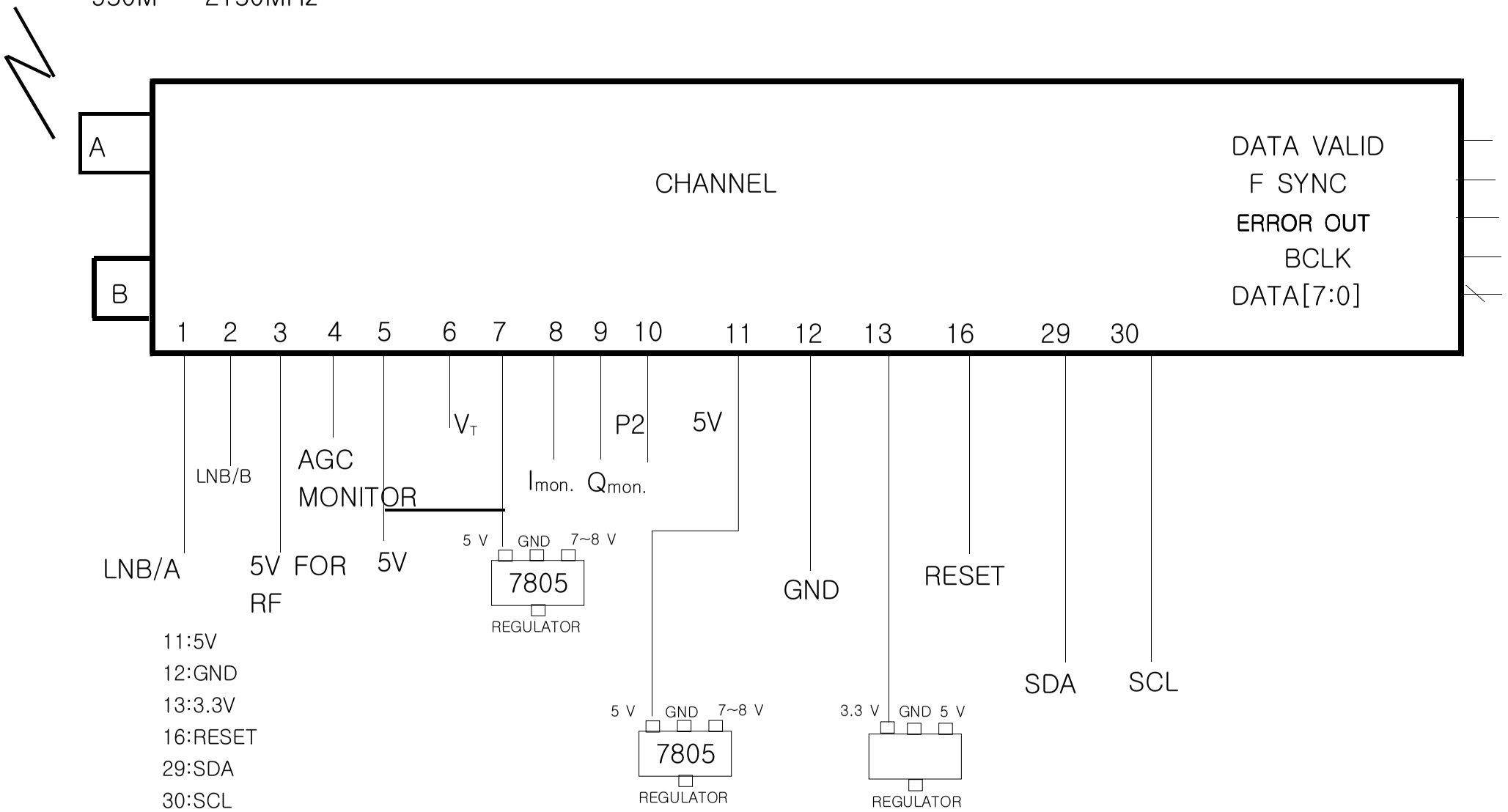
7.3. Schematic diagram of SMPS (power supply)

- See the Schematic Diagram Section < SMPS Board > -- A. 4

A. 1 Pin description Section

9. APPLICATION CIRCUIT

950M ~ 2150MHz



National Semiconductor

February 1995

LM78XX Series Voltage Regulators

General Description

The LM78XX series of three terminal regulators is available with several fixed output voltages making them useful in a wide range of applications. One of these is local on card regulation, eliminating the distribution problems associated with single point regulation. The voltages available allow these regulators to be used in logic systems, instrumentation, HiFi, and other solid state electronic equipment. Although designed primarily as fixed voltage regulators these devices can be used with external components to obtain adjustable voltages and currents.

The LM78XX series is available in an aluminum TO-3 package which will allow over 1.0A load current if adequate heat sinking is provided. Current limiting is included to limit the peak output current to a safe value. Safe area protection for the output transistor is provided to limit internal power dissipation. If internal power dissipation becomes too high for the heat sinking provided, the thermal shutdown circuit takes over preventing the IC from overheating.

Considerable effort was expanded to make the LM78XX series of regulators easy to use and mininize the number of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor of the power supply.

For output voltage other than 5V, 12V and 15V the LM117 series provides an output voltage range from 1.2V to 57V.

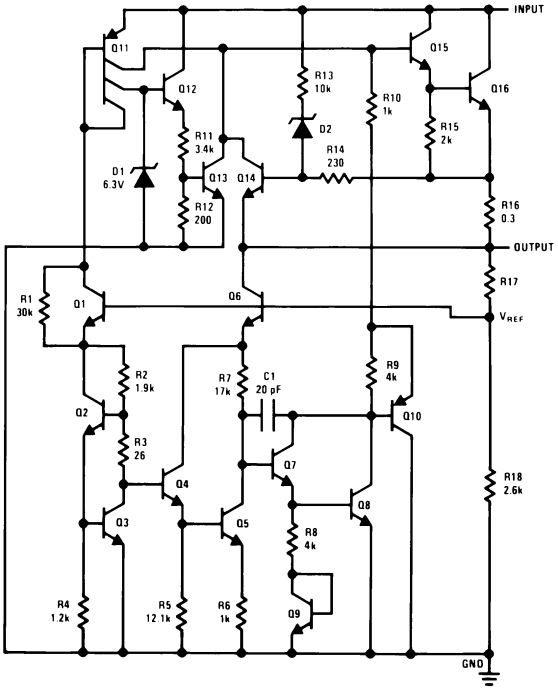
Features

- Output current in excess of 1A
- Internal thermal overload protection
- No external components required
- Output transistor safe area protection
- Internal short circuit current limit
- Available in the aluminum TO-3 package

Voltage Range

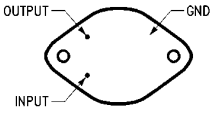
LM7805C	5V
LM7812C	12V
LM7815C	15V

Schematic and Connection Diagrams



TL/H/7746–1

Metal Can Package
TO-3 (K)
Aluminum

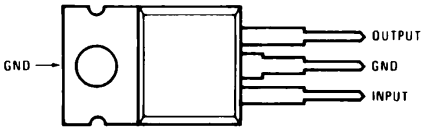


TL/H/7746–2

Bottom View

Order Number LM7805CK,
LM7812CK or LM7815CK
See NS Package Number KC02A

Plastic Package
TO-220 (T)



TL/H/7746–3

Top View

Order Number LM7805CT,
LM7812CT or LM7815CT
See NS Package Number T03B

RRD-B30M115/Printed in U. S. A.

input pin is available (EXTM). An appropriate DC blocking capacitor must be used to couple the modulating signal source to the EXTM pin. When external modulation is not used, the relevant pin can be left open.
Two pins are dedicated to the overcurrent protection/monitoring: CEXT and OLF. The overcurrent protection circuit works dynamically: as soon as an overload is detected in either LNB output, the output is shut-down for a time t_{off} determined by the capacitor connected between CEXT and GND. Simultaneously the OLF pin, that is an open collector diagnostic output flag, from HIGH IMPEDANCE state goes LOW.
After the time has elapsed, the output is resumed for a time $t_{on}=1/15t_{off}$ (typ.) and OLF goes in HIGH IMPEDANCE. If the overload is still present, the protection circuit will cycle again through t_{off} and ton until the overload is removed. Typical $t_{on}+t_{off}$ value is 1200ms when a 4.7µF external capacitor is used.
This dynamic operation can greatly reduce the power dissipation in short circuit condition, still ensuring excellent power-on start up even with highly capacitive loads on LNB outputs.
The device is packaged in Multiwatt15 for thru-holes mounting and in PowerSO-20 for surface mounting. When a limited functionality in a smaller package matches design needs, a range of cost-effective PowerSO-10 solutions is also offered. All versions have built-in thermal protection against overheating damage.

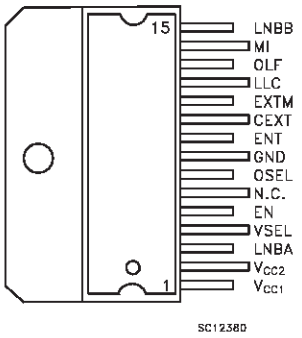
(*): External components are needed to comply to level 2.x and above (bidirectiona) DiSEqC™ bus hardware requirements. DiSEqC™ is a trademark or EUTELSAT.

ORDERING CODES

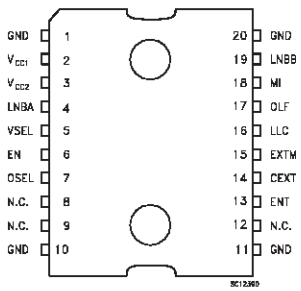
TYPE	Multiwatt-15	PowerSO-20	PowerSO-10
LNBP10			LNBP10SP-TR (*)
LNBP11			LNBP11SP-TR (*)
LNBP12			LNBP12SP-TR (*)
LNBP13			LNBP13SP-TR (*)
LNBP14			LNBP14SP-TR (*)
LNBP15			LNBP15SP-TR (*)
LNBP16			LNBP16SP-TR (*)
LNBP20	LNBP20CR	LNBP20PD-TR	

(*) Available on request

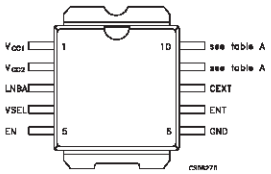
PIN CONFIGUARATION (top view)



Multiwatt-15



PowerSo-20



PowerSO-10

1.6.4.10 Real Time Clock / Front Panel Controller

The Real Time Clock (RTC) counts seconds, minutes, hours, and days. Programmable alarms can be set to interrupt the CPU, allowing the CPU to wake up when required to perform functions such as programming a VCR. The RTC also provides a 3-wire front panel control interface, to drive panel seven-segment displays with time information or program-generated data.

1.7 Signal and I/O Information

1.7.1 Signals Sorted by Signal Name

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
27MHZ_CLK	AA12
AUD_GNDA0	AA8
AUD_GNDA1	AC3
AUD_VDDA0	AC7
AUD_VDDA1	AA5
BI_ADDRESS11	W21
BI_ADDRESS12	Y23
BI_ADDRESS13	V20
BI_ADDRESS14	W22
BI_ADDRESS15	W23
BI_ADDRESS16	V21
BI_ADDRESS17	V22
BI_ADDRESS18	U21
BI_ADDRESS19	U22
BI_ADDRESS20	U23
BI_ADDRESS21	T21
BI_ADDRESS22	P20
BI_ADDRESS23	R23
BI_ADDRESS24	P21
BI_ADDRESS25	P22
BI_ADDRESS26	N21
BI_ADDRESS27	N22
BI_ADDRESS28	N23
BI_ADDRESS29	M21
BI_ADDRESS30	M22
BI_ADDRESS31_$\overline{\text{WBE1}}$	M23

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
$\overline{\text{BI\_CS0}}$	D21
$\overline{\text{BI\_CS1}}$	C22
$\overline{\text{BI\_CS2}}$	B23
$\overline{\text{BI\_CS3}}$	A22
BI_DATA0	L23
BI_DATA1	L22
BI_DATA10	G23
BI_DATA11	G22
BI_DATA12	G21
BI_DATA13	F22
BI_DATA14	E23
BI_DATA15	E22
BI_DATA2	L21
BI_DATA3	K21
BI_DATA4	K20
BI_DATA5	J23
BI_DATA6	J22
BI_DATA7	J21
BI_DATA8	H23
BI_DATA9	H22
$\overline{\text{BI\_OE}}$	B20
BI_READY	D23
BI_$\overline{\text{RW}}$	E21
$\overline{\text{BI\_WBE0}}$	C23
CI_CLOCK	B5
CI_DATA0	B19
CI_DATA1	B18
CI_DATA2	C16
CI_DATA3	A16
CI_DATA4	A12
CI_DATA5	D14
CI_DATA6	B14
CI_DATA7	B12
CI_DATA_ENABLE	C6
CLK_GNDA	AC9
CLK_VDDA	AA11
DAC1_AGND0	B7

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
DAC1_AGND1	A8
DAC1_AGND2	C10
DAC1_AVDD0	B6
DAC1_AVDD1	C8
DAC1_AVDD2	A9
DAC1_AVDD3	B11
DAC1_VOUT1	C7
DAC1_CREF_OUT	A5
DAC1_VOUT2	B8
DAC1_GREF_OUT	A11
DAC1_VOUT3	C11
DAC1_RREF_OUT	A7
DAC1_VREF_IN	C9
DAC2_AGND0	B17
DAC2_AGND1	B15
DAC2_AGND2	C13
DAC2_AVDD0	C18
DAC2_AVDD1	B16
DAC2_AVDD2	C14
DAC2_AVDD3	A13
DAC2_VOUT1	C17
DAC2_CREF_OUT	A19
DAC2_VOUT2	C15
DAC2_GREF_OUT	C12
DAC2_VOUT3	B13
DAC2_RREF_OUT	A17
DAC2_VREF_IN	A15
DA_BIT_CLOCK	AA1
DA_LR_CLOCK	AA2
DA_OS_CLOCK	AB1
DA_SERIAL_DATA	Y2
DA_SPDIF	F20
$\overline{\text{EDMAC3\_ACK}}$/DMACK	V3
$\overline{\text{EDMAC3\_REQ}}$/DMARQ	E3
GND	A01
GND	A06
GND	A10

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
GND	A14
GND	A18
GND	A23
GND	AA3
GND	AB2
GND	AC1
GND	AC6
GND	B02
GND	B22
GND	C03
GND	C21
GND	D04
GND	D08
GND	D12
GND	D16
GND	D20
GND	F01
GND	F23
GND	H04
GND	H20
GND	K01
GND	K23
GND	M04
GND	M20
GND	P01
GND	P23
GND	T04
GND	T20
GND	V01
GND	V23
GND	Y04
GND	Y08
GND	Y12
GND	Y16
GND	Y20
GND	AA21
GND	AA23

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
GND	AB22
GND	AC10
GND	AC14
GND	AC18
GND	AC23
GPIO0	AC2
GPIO1	Y6
GPIO10	R2
GPIO11	V4
GPIO12	W3
GPIO13	B10
GPIO14	D10
GPIO15	B9
GPIO16	D6
GPIO17	C5
GPIO18	A4
GPIO19	B4
GPIO2	AB5
GPIO20	A3
GPIO21	C4
GPIO22	B3
GPIO23	A2
GPIO24	K22
GPIO25	Y22
GPIO26	Y21
GPIO27	AA6
GPIO28	F21
GPIO29	R22
GPIO3	AC5
GPIO30	K4
GPIO31	AB15
GPIO4	C19
GPIO5	B21
GPIO6	A20
GPIO7	D18
GPIO8	M1
GPIO9	N3

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
I2C0_SCL	T3
I2C0_SDA	U3
$\overline{\text{INT0}}$	C1
$\overline{\text{INT1}}$	D2
$\overline{\text{INT2}}$	H21
$\overline{\text{RW\_HALT}}$	W2
RW_TCK	AB6
RW_TDI	AC4
RW_TDO	Y1
RW_TMS	AB3
$\overline{\text{RW\_TRST}}$	Y3
SC0_CLK	AA9
SC0_DETECT	AB4
SC0_IO	AB8
SC0_RESET	AC8
SC0_VCC	C20
SC1_CLK	R21
SC1_DETECT	T22
SC1_IO	D22
SC1_RESET	T23
SC1_VCC	A21
SD0_ADDRESS0	M3
SD0_ADDRESS1	M2
SD0_ADDRESS10	T2
SD0_ADDRESS11	U1
SD0_ADDRESS12	U2
SD0_ADDRESS13	V2
SD0_ADDRESS14	W1
SD0_ADDRESS2	N1
SD0_ADDRESS3	N2
SD0_ADDRESS4	P2
SD0_ADDRESS5	P3
SD0_ADDRESS6	P4
SD0_ADDRESS7	R1
SD0_ADDRESS8	R3
SD0_ADDRESS9	T1
$\overline{\text{SD0\_CAS}}$	L3

Table 1-1. Signals Sorted by Signal Name

I/O Signal Name	Ball Number
SD0_CLK	J1
$\overline{\text{SD0\_CS0}}$	J2
SD0_DATA0	B1
SD0_DATA1	C2
SD0_DATA10	G2
SD0_DATA11	G1
SD0_DATA12	H3
SD0_DATA13	H2
SD0_DATA14	H1
SD0_DATA15	J3
SD0_DATA2	D3
SD0_DATA3	D1
SD0_DATA4	F4
SD0_DATA5	E2
SD0_DATA6	E1
SD0_DATA7	F3
SD0_DATA8	F2
SD0_DATA9	G3
SD0_DQMH	L2
SD0_DQML	L1
$\overline{\text{SD0\_RAS}}$	K2
$\overline{\text{SD0\_WE}}$	K3
SD1_ADDRESS0	AA17
SD1_ADDRESS1	AB18
SD1_ADDRESS10	AA20
SD1_ADDRESS11	AB21
SD1_ADDRESS12	AC22
SD1_ADDRESS13	AB23
SD1_ADDRESS14	AA22
SD1_ADDRESS2	AA18
SD1_ADDRESS3	AC19
SD1_ADDRESS4	AB19
SD1_ADDRESS5	Y18
SD1_ADDRESS6	AA19
SD1_ADDRESS7	AC20
SD1_ADDRESS8	AB20
SD1_ADDRESS9	AC21

Table 1-1. Signals Sorted by Signal Name

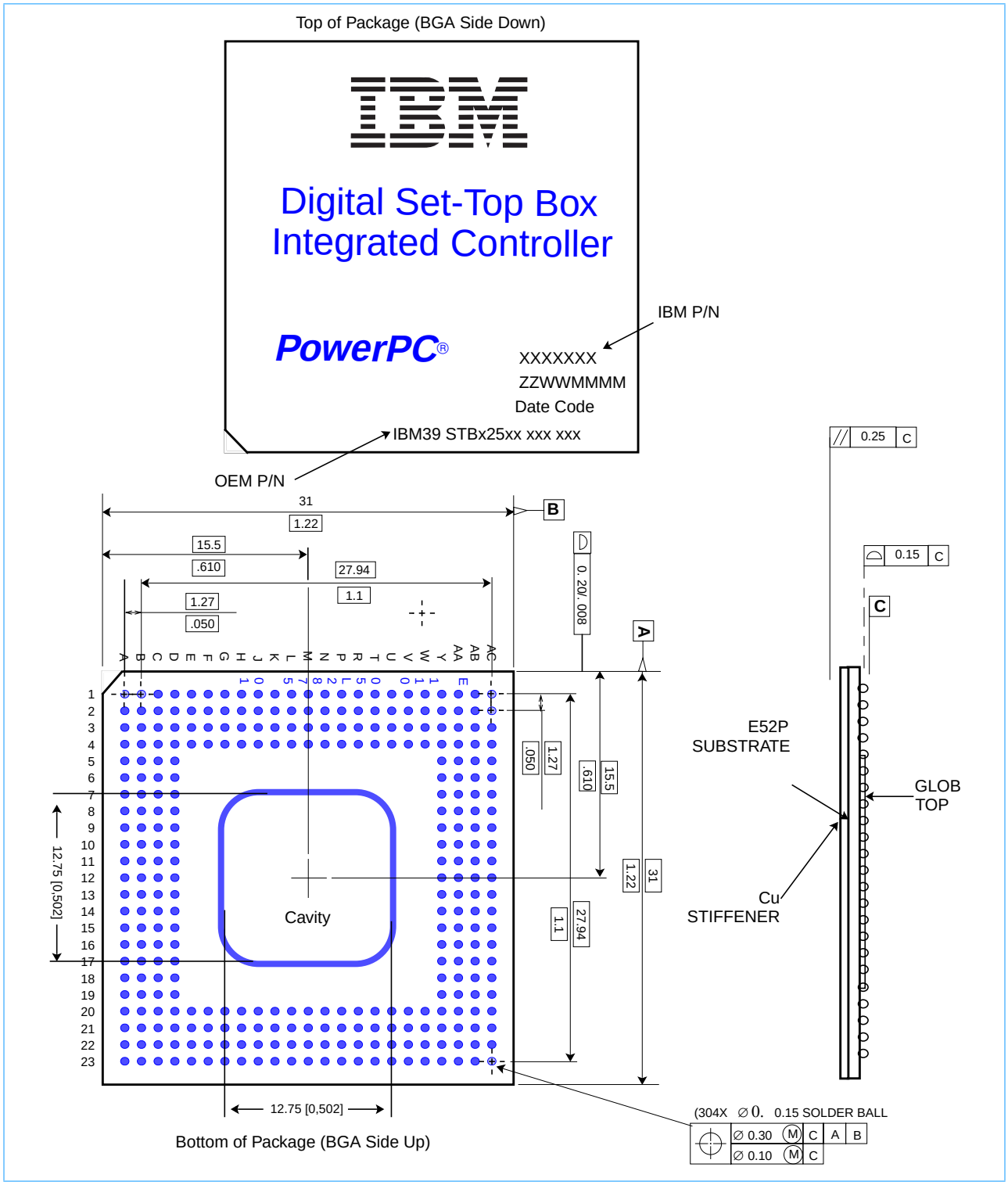
I/O Signal Name	Ball Number
SD1_CAS	AA16
SD1_CLK	AA15
SD1_CS0	AC15
SD1_DATA0	AA7
SD1_DATA1	AB7
SD1_DATA10	AC13
SD1_DATA11	AB13
SD1_DATA12	AA13
SD1_DATA13	AB14
SD1_DATA14	AA14
SD1_DATA15	Y14
SD1_DATA2	AB9
SD1_DATA3	Y10
SD1_DATA4	AA10
SD1_DATA5	AB10
SD1_DATA6	AB11
SD1_DATA7	AC11
SD1_DATA8	AB12
SD1_DATA9	AC12
SD1_DQMH	AC17
SD1_DQML	AB17
$\overline{\text{SD1\_RAS}}$	AB16
$\overline{\text{SD1\_WE}}$	AC16
$\overline{\text{SYSTEM\_RESET}}$	AA4
VDD	D09
VDD	D11
VDD	D13
VDD	D15
VDD	J04
VDD	J20
VDD	L04
VDD	L20
VDD	N04
VDD	N20
VDD	R04
VDD	R20
VDD	Y09

Table 1-1. Signals Sorted by Signal Name

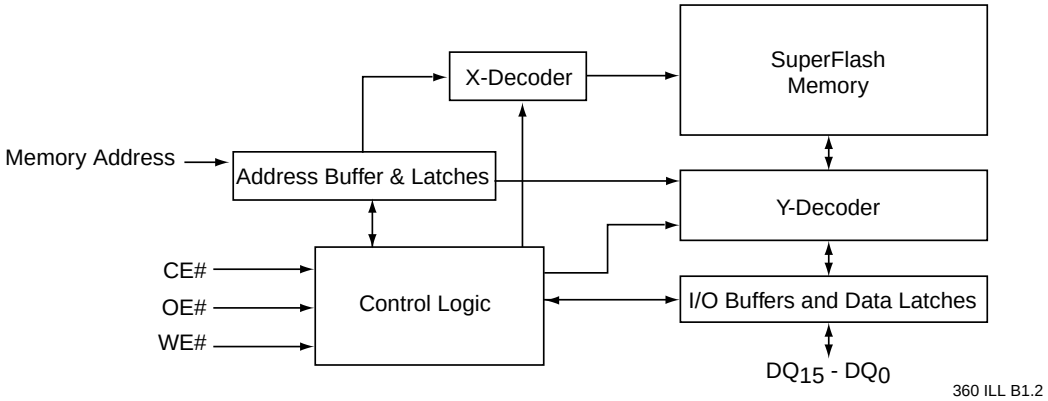
I/O Signal Name	Ball Number
VDD	Y11
VDD	Y13
VDD	Y15
VDD2	D05
VDD2	D07
VDD2	D17
VDD2	D19
VDD2	E04
VDD2	E20
VDD2	G04
VDD2	G20
VDD2	U04
VDD2	U20
VDD2	W04
VDD2	W20
VDD2	Y05
VDD2	Y07
VDD2	Y17
VDD2	Y19

1.10 Mechanical Information

Package Diagram



FUNCTIONAL BLOCK DIAGRAM



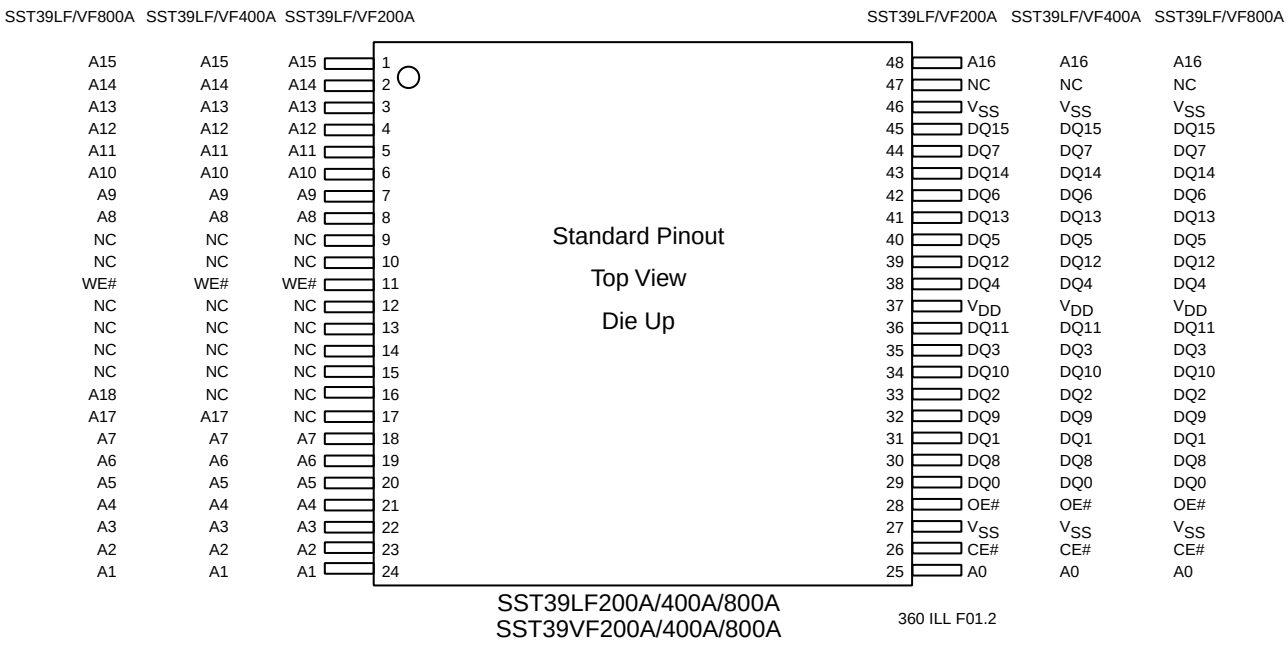


FIGURE 1: PIN ASSIGNMENTS FOR 48-LEAD TSOP

Features

- **Low-Voltage and Standard-Voltage Operation**
  - **5.0 (V_{CC} = 4.5V to 5.5V)**
  - **2.7 (V_{CC} = 2.7V to 5.5V)**
  - **2.5 (V_{CC} = 2.5V to 5.5V)**
  - **1.8 (V_{CC} = 1.8V to 5.5V)**
- **Internally Organized 128 x 8 (1K), 256 x 8 (2K), 512 x 8 (4K), 1024 x 8 (8K) or 2048 x 8 (16K)**
- **2-Wire Serial Interface**
- **Schmitt Trigger, Filtered Inputs for Noise Suppression**
- **Bidirectional Data Transfer Protocol**
- **100 kHz (1.8V, 2.5V, 2.7V) and 400 kHz (5V) Compatibility**
- **Write Protect Pin for Hardware Data Protection**
- **8-Byte Page (1K, 2K), 16-Byte Page (4K, 8K, 16K) Write Modes**
- **Partial Page Writes Are Allowed**
- **Self-Timed Write Cycle (10 ms max)**
- **High Reliability**
  - **Endurance: 1 Million Write Cycles**
  - **Data Retention: 100 Years**
  - **ESD Protection: >3000V**
- **Automotive Grade and Extended Temperature Devices Available**
- **8-Pin and 14-Pin JEDEC SOIC, 8-Pin PDIP, 8-Pin MSOP, and 8-Pin TSSOP Packages**

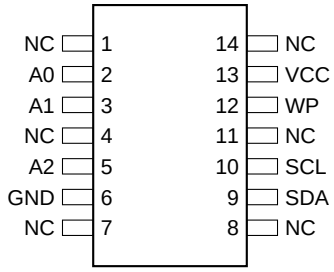
Description

The AT24C01A/02/04/08/16 provides 1024/2048/4096/8192/16384 bits of serial electrically erasable and programmable read only memory (EEPROM) organized as 128/256/512/1024/2048 words of 8 bits each. The device is optimized for use in many industrial and commercial applications where low power and low voltage operation are essential. The AT24C01A/02/04/08/16 is available in space saving 8-pin PDIP, (AT24C01A/02/04/08/16), 8-Pin MSOP (AT24C01A/02), 8-Pin TSSOP (AT24C01A/02/04/08/16), and 8-Pin and 14-Pin JEDEC SOIC (AT24C01A/02/04/08/16) packages and is accessed via a 2-wire serial interface. In addition, the entire family is available in 5.0V (4.5V to 5.5V), 2.7V (2.7V to 5.5V), 2.5V (2.5V to 5.5V) and 1.8V (1.8V to 5.5V) versions.

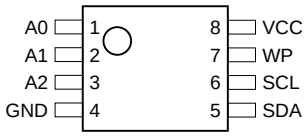
Pin Configurations

Pin Name	Function
A0 - A2	Address Inputs
SDA	Serial Data
SCL	Serial Clock Input
WP	Write Protect
NC	No Connect

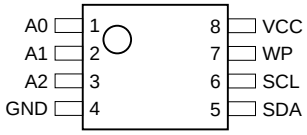
14-Pin SOIC



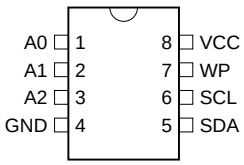
8-Pin TSSOP



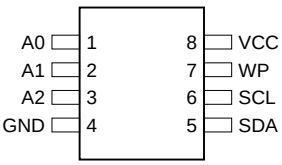
8-Pin MSOP



8-Pin PDIP



8-Pin SOIC



2-Wire Serial EEPROM

1K (128 x 8)

2K (256 x 8)

4K (512 x 8)

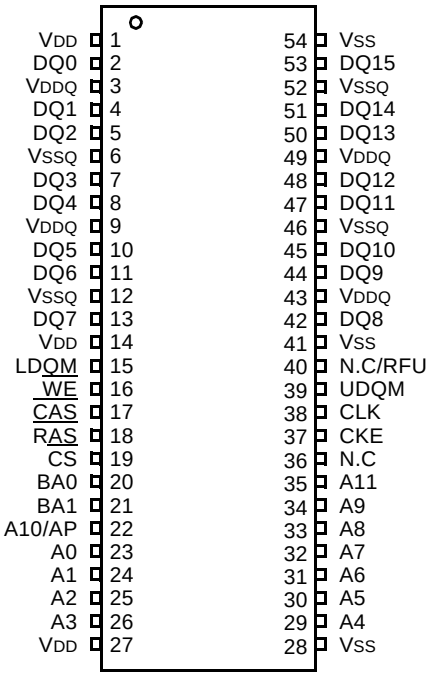
8K (1024 x 8)

16K (2048 x 8)

AT24C01A
AT24C02
AT24C04
AT24C08
AT24C16

Rev. 0180D–10/98

PIN CONFIGURATION (Top view)



54Pin TSOP (II)
(400mil x 875mil)
(0.8 mm Pin pitch)

PIN FUNCTION DESCRIPTION

Pin	Name	Input Function
CLK	*System clock*	Active on the positive going edge to sample all inputs.
$\overline{CS}$	*Chip select*	Disables or enables device operation by masking or enabling all inputs except CLK, CKE and L(U)DQM
CKE	*Clock enable*	Masks system clock to freeze operation from the next clock cycle. CKE should be enabled at least one cycle prior to new command. Disable input buffers for power down in standby.
A_0 ~ A_{11}	*Address*	Row/column addresses are multiplexed on the same pins. Row address : RA_0 ~ RA_{11}, Column address : CA_0 ~ CA_7
BA_0 ~ BA_1	*Bank select address*	Selects bank to be activated during row address latch time. Selects bank for read/write during column address latch time.
$\overline{RAS}$	*Row address strobe*	Latches row addresses on the positive going edge of the CLK with $\overline{RAS}$ low. Enables row access & precharge.
$\overline{CAS}$	*Column address strobe*	Latches column addresses on the positive going edge of the CLK with $\overline{CAS}$ low. Enables column access.
$\overline{WE}$	*Write enable*	Enables write operation and row precharge. Latches data in starting from $\overline{CAS}$, $\overline{WE}$ active.
L(U)DQM	*Data input/output mask*	Makes data output Hi-Z, t_{SHZ} after the clock and masks the output. Blocks data input when L(U)DQM active.
$DQ_{0 \sim 15}$	*Data input/output*	Data inputs/outputs are multiplexed on the same pins.
V_{DD}/V_{SS}	*Power supply/ground*	Power and ground for the input buffers and the core logic.
V_{DDQ}/V_{SSQ}	*Data output power/ground*	Isolated power supply and ground for the output buffers to provide improved noise immunity.
N.C/RFU	*No connection /reserved for future use*	This pin is recommended to be left No Connection on the device.

ELM97xxxxA VOLTAGE DETECTOR

■ GENERAL DESCRIPTION

ELM 97xxxxA Series is a CMOS Voltage Detector IC for battery-operated portable devices. It consists of a very low-power-consumption reference voltage source, a comparator, an output driver, a hysteresis circuit, and detection voltage setting resistors. Output logic is positive, therefore output level is low when VDD is lower than detection voltage.

It can be used as a reset controller in microcomputer-based systems. And it can be widely applied to the devices, such as battery checkers, switching circuit of back-up power source, power failure detectors, etc. Two output styles are available, N-ch opendrain and CMOS output.

It is available in SOT-89 and SOT-23.

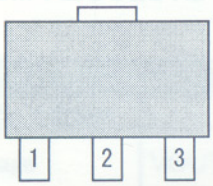
■ FEATURES

- Low power operation : TYP. 1.0 μ A (VDD = 1.5V)
- Low voltage operation : Reset operation assured at 0.8V
- High accuracy of detection voltage : $\pm$ 2.5%
- Low temperature coefficient : TYP. −300ppm/°C (Detection voltage $<$ 2.0V)
  : TYP. −100ppm/°C (Detection voltage $\geqq$ 2.0V)
- Very small package : SOT-89, SOT-23

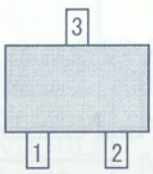
■ APPLICATION

- Reset for microcomputer
- Battery checker
- Power failure detector
- Switching of back-up power source

■ PIN CONFIGURATION (TOP VIEW)



SOT-89

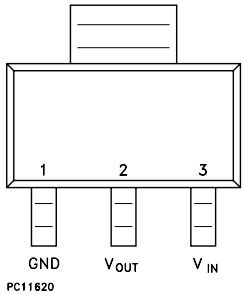


SOT-23

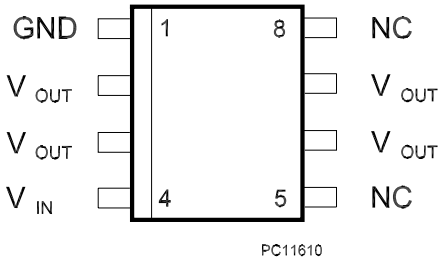
Pin No.	Pin Name
1	OUT
2	VDD
3	VSS

Pin No.	Pin Name
1	OUT
2	VSS
3	VDD

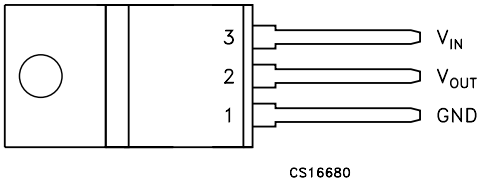
CONNECTION DIAGRAM (top view)



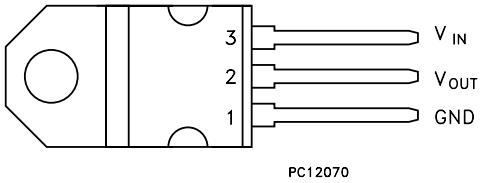
SOT-223



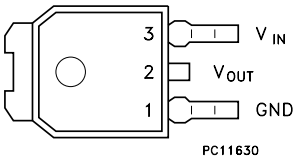
SO-8



TO-220FM



TO-220



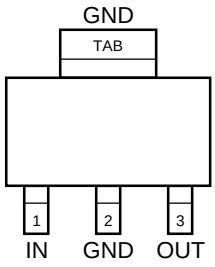
DPAK

NOTE: The TAB is connected to the V_{OUT}.

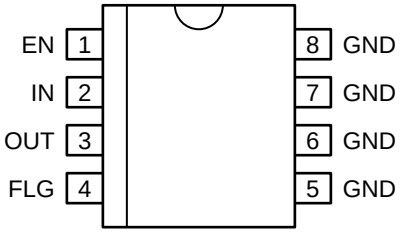
ORDERING CODES

SOT-223	SO-8	DPAK	TO-220	TO-220FM	OUTPUT VOLTAGE
LD1117S12	LD1117D12 (*)	LD1117DT12	LD1117V12 (*)	LD1117F12 (*)	1.2 V
LD1117S18	LD1117D18	LD1117DT18	LD1117V18	LD1117F18	1.8 V
LD1117S18C	LD1117D18C	LD1117DT18C	LD1117V18C	LD1117F18C	1.8 V
LD1117S25	LD1117D25	LD1117DT25	LD1117V25	LD1117F25	2.5 V
LD1117S25C	LD1117D25C	LD1117DT25C	LD1117V25C	LD1117F25C	2.5 V
LD1117S28	LD1117D28	LD1117DT28	LD1117V28	LD1117F28	2.85 V
LD1117S30	LD1117D30	LD1117DT30	LD1117V30	LD1117F30	3 V
LD1117S30C	LD1117D30C	LD1117DT30C	LD1117V30C	LD1117F30C	3 V
LD1117S33	LD1117D33	LD1117DT33	LD1117V33	LD1117F33	3.3 V
LD1117S33C	LD1117D33C	LD1117DT33C	LD1117V33C	LD1117F33C	3.3 V
LD1117S50	LD1117D50	LD1117DT50	LD1117V50	LD1117F50	5 V
LD1117S50C	LD1117D50C	LD1117DT50C	LD1117V50C	LD1117F50C	5 V
LD1117S	LD1117D	LD1117DT	LD1117V	LD1117F	ADJUSTABLE FROM 1.25 TO 15V
LD1117SC	LD1117DC	LD1117DTC	LD1117VC	LD1117FC	ADJUSTABLE FROM 1.25 TO 15V

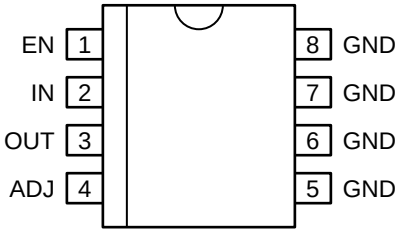
Pin Configuration



MIC39100-x.x
Fixed
SOT-223 (S)



MIC39101-x.x
Fixed
SOP-8 (M)

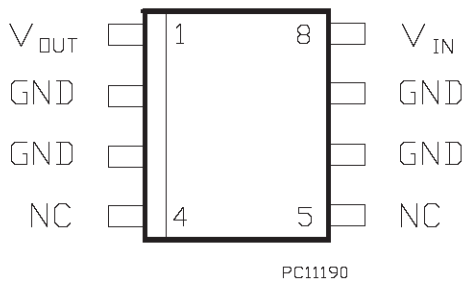


MIC39102
Adjustable
SOP-8 (M)

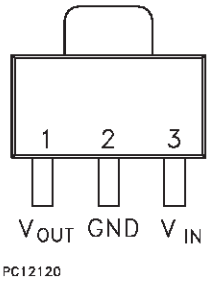
Pin Description

Pin No. MIC39100	Pin No. MIC39101	Pin No. MIC39102	Pin Name	Pin Function
1	1	1	EN	Enable (Input): CMOS-compatible control input. Logic high = enable, logic low or open = shutdown.
	2	2	IN	Supply (Input)
3	3	3	OUT	Regulator Output
	4		FLG	Flag (Output): Open-collector error flag output. Active low = output under-voltage.
		4	ADJ	Adjustment Input: Feedback input. Connect to resitive voltage-divider network.
2, TAB	5–8	5–8	GND	Ground

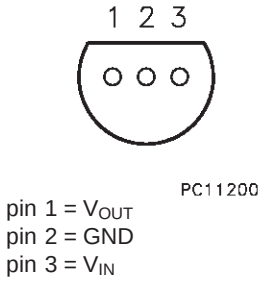
CONNECTION DIAGRAM AND ORDERING NUMBERS (top view)



SO-8



SOT-89

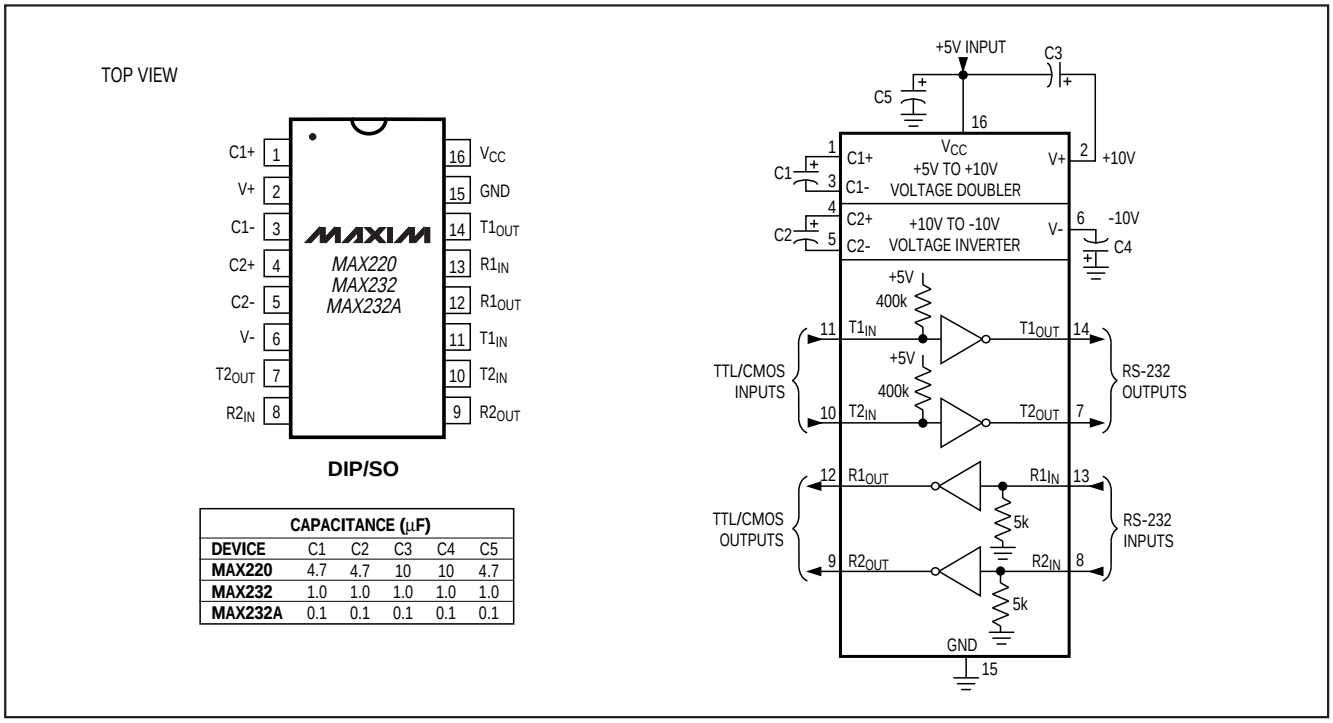


TO-92

BOTTOM VIEW

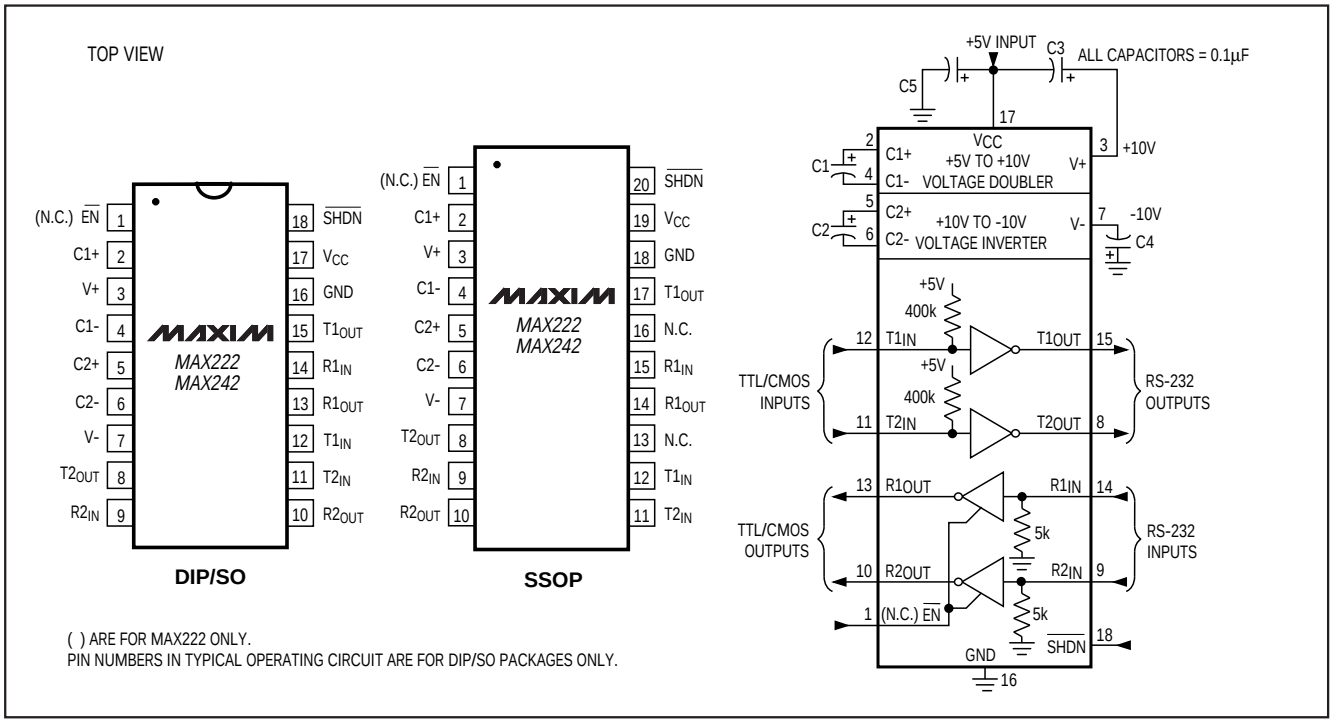
ORDERING NUMBERS

Type	SO-8	TO-92	SOT-89 (T&R)	Output Voltage
L78L33AC	L78L33ACD	L78L33ACZ	L78L33ACUTR	3.3 V
L78L33AB	L78L33ABD	L78L33ABZ	L78L33ABUTR	3.3 V
L78L05C	L78L05CD	L78L05CZ		5 V
L78L05AC	L78L05ACD	L78L05ACZ	L78L05ACUTR	5 V
L78L05AB	L78L05ABD	L78L05ABZ	L78L05ABUTR	5 V
L78L06C	L78L06CD	L78L06CZ		6 V
L78L06AC	L78L06ACD	L78L06ACZ	L78L06ACUTR	6 V
L78L06AB	L78L06ABD	L78L06ABZ	L78L06ABUTR	6 V
L78L08C	L78L08CD	L78L08CZ		8 V
L78L08AC	L78L08ACD	L78L08ACZ	L78L08ACUTR	8 V
L78L08AB	L78L08ABD	L78L08ABZ	L78L08ABUTR	8 V
L78L09C	L78L09CD	L78L09CZ		9 V
L78L09AC	L78L09ACD	L78L09ACZ	L78L09ACUTR	9 V
L78L09AB	L78L09ABD	L78L09ABZ	L78L09ABUTR	9 V
L78L12C	L78L12CD	L78L12CZ		12 V
L78L12AC	L78L12ACD	L78L12ACZ	L78L12ACUTR	12 V
L78L12AB	L78L12ABD	L78L12ABZ	L78L12ABUTR	12 V
L78L15C	L78L15CD	L78L15CZ		15 V
L78L15AC	L78L15ACD	L78L15ACZ	L78L15ACUTR	15 V
L78L15AB	L78L15ABD	L78L15ABZ	L78L15ABUTR	15 V
L78L18C	L78L18CD	L78L18CZ		18 V
L78L18AC	L78L18ACD	L78L18ACZ	L78L18ACUTR	18 V
L78L18AB	L78L18ABD	L78L18ABZ	L78L18ABUTR	18 V
L78L24C	L78L24CD	L78L24CZ		24 V
L78L24AC	L78L24ACD	L78L24ACZ	L78L24ACUTR	24 V
L78L24AB	L78L24ABD	L78L24ABZ	L78L24ABUTR	24 V



CAPACITANCE (µF)					
DEVICE	C1	C2	C3	C4	C5
MAX220	4.7	4.7	10	10	4.7
MAX232	1.0	1.0	1.0	1.0	1.0
MAX232A	0.1	0.1	0.1	0.1	0.1

Figure 5. MAX220/MAX232/MAX232A Pin Configuration and Typical Operating Circuit



( ) ARE FOR MAX222 ONLY.
PIN NUMBERS IN TYPICAL OPERATING CIRCUIT ARE FOR DIP/SO PACKAGES ONLY.

Figure 6. MAX222/MAX242 Pin Configurations and Typical Operating Circuit

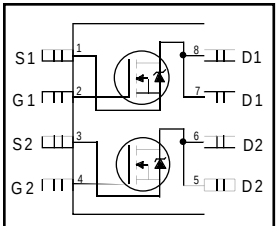
International
IOR Rectifier

PD - 9.1239D

IRF7303

HEXFET® Power MOSFET

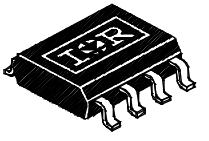
- Generation V Technology
- Ultra Low On-Resistance
- Dual N-Channel Mosfet
- Surface Mount
- Available in Tape & Reel
- Dynamic dv/dt Rating
- Fast Switching



Top View

V_{DSS} = 30V

$R_{DS(on)}$ = 0.050Ω


SO-8

Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve the lowest possible on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques. Power dissipation of greater than 0.8W is possible in a typical PCB mount application.

Absolute Maximum Ratings

	Parameter	Max.	Units
I_D @ T_A = 25°C	10 Sec. Pulsed Drain Current, V_{GS} @ 10V	5.3	A
I_D @ T_A = 25°C	Continuous Drain Current, V_{GS} @ 10V	4.9	
I_D @ T_A = 70°C	Continuous Drain Current, V_{GS} @ 10V	3.9	
I_{DM}	Pulsed Drain Current ①	20	
P_D @T_A = 25°C	Power Dissipation	2.0	W
	Linear Derating Factor	0.016	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
dv/dt	Peak Diode Recovery dv/dt ②	5.0	V/ns
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance Ratings

	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient④	——	62.5	°C/W

7 PINNING

SYMBOL	PIN	PAD TYPE	DESCRIPTION
BCK	1	5 V tolerant digital input pad; note 1	bit clock input
WS	2	5 V tolerant digital input pad; note 1	word select input
DATAI	3	5 V tolerant digital input pad; note 1	serial data input
V_{DDD}	4	digital supply pad	digital supply voltage
V_{SSD}	5	digital ground pad	digital ground
SYSCLK	6	5 V tolerant digital input pad; note 1	system clock input
SFOR1	7	5 V tolerant digital input pad; note 1	serial format select 1
MUTE	8	5 V tolerant digital input pad; note 1	mute control
DEEM	9	5 V tolerant digital input pad; note 1	de-emphasis control
PCS	10	3-level input pad; note 2	power control and sampling frequency select
SFOR0	11	digital input pad; note 2	serial format select 0
$V_{ref(DAC)}$	12	analog pad	DAC reference voltage
V_{DDA}	13	analog supply pad	DAC analog supply voltage
VOUTL	14	analog output pad	DAC output left
V_{SSA}	15	analog ground pad	DAC analog ground
VOUTR	16	analog output pad	DAC output right

Notes

1. 5 V tolerant is only supported if the power supply voltage is between 2.7 and 3.6 V. For lower power supply voltages this is maximum 3.3 V tolerant.
2. Because of test issues these pads are not 5 V tolerant and they should be at power supply voltage level or at a maximum of 0.5 V above that level.

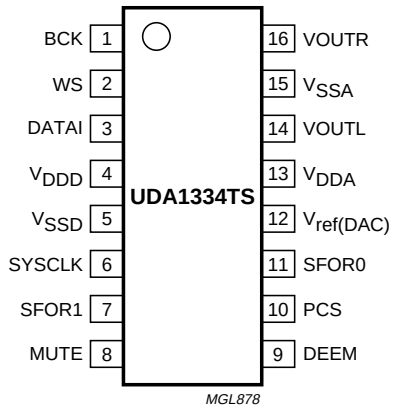
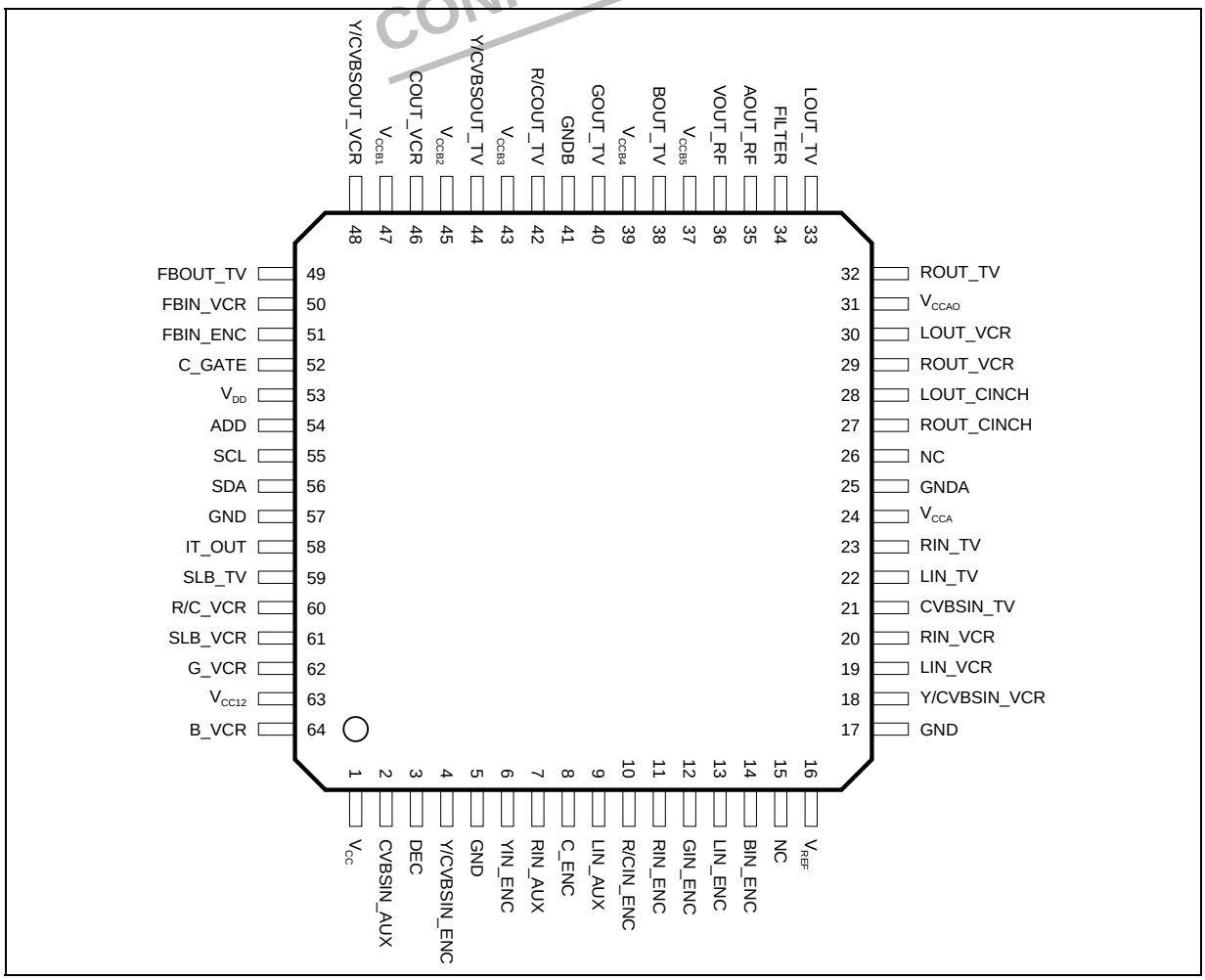


Fig.2 Pin configuration.

PIN CONNECTIONS



6412-01.EPS

PIN LIST

Pin Number	Symbol	Description
1	V_{CC}	+5V Supply
2	CVBSIN_AUX	CVBS Input, from Aux
3	DEC	Decoupling Capacitor
4	Y/CVBSIN_ENC	Y/CVBS Input, from Encoder
5	GND	Ground
6	YIN_ENC	Y Input, from Encoder
7	RIN_AUX	Audio Right Input, from Aux
8	CIN_ENC	Chroma Input, from Encoder
9	LIN_AUX	Audio Left, Input from Aux
10	R/CIN_ENC	Red/Chroma Input, from Encoder
11	RIN_ENC	Audio Right, Input from Encoder
12	GIN_ENC	Green Input, from Encoder
13	LIN_ENC	Audio Left, Input from Encoder
14	BIN_ENC	Blue Input, from Encoder
15	NC	Not Connected
16	V_{REF}	Reference Voltage Decoupling

6412-01.TBL

PIN LIST (continued)

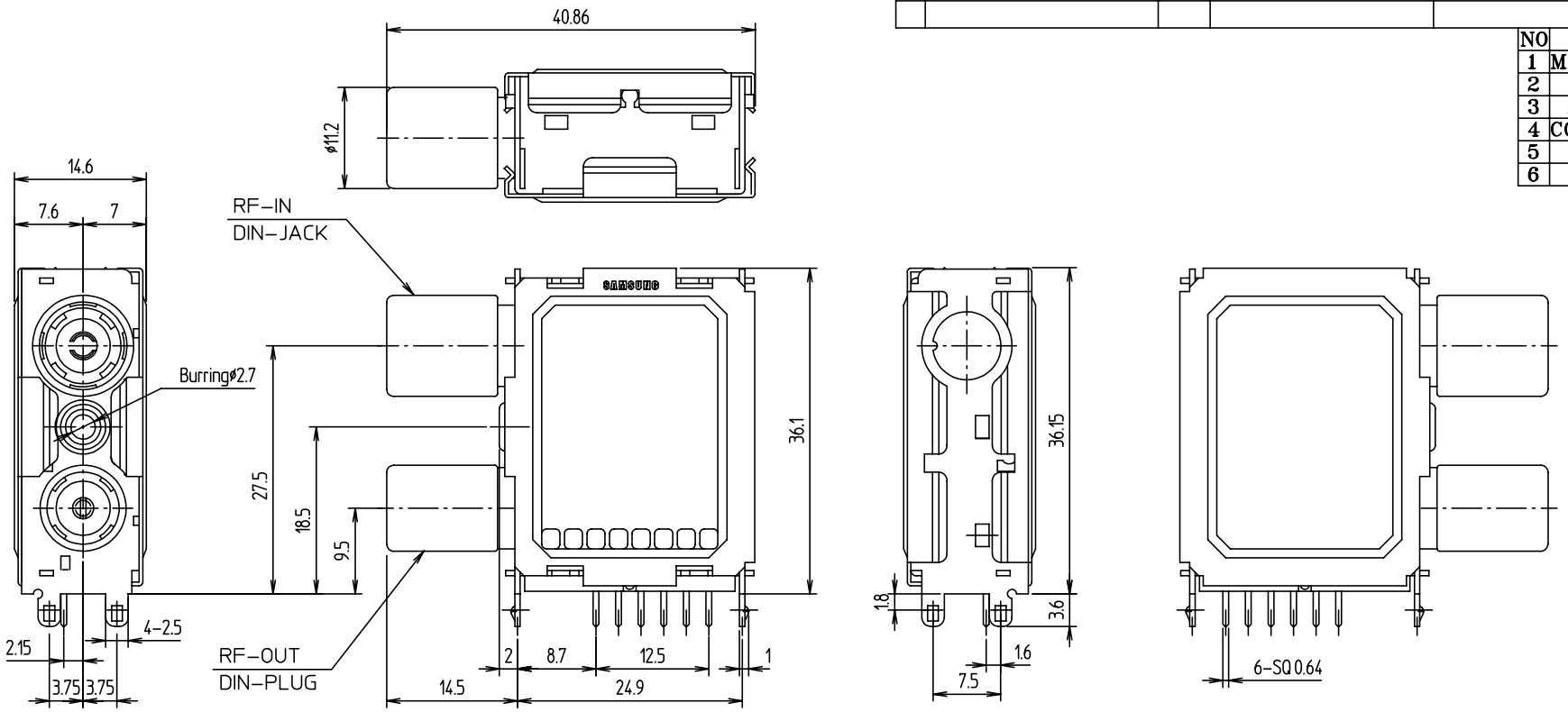
Pin Number	Symbol	Description
17	GND	Ground
18	Y/CVBSIN_VCR	Y/CVBS Input, from VCR scart
19	LIN_VCR	Audio Left, Input from VCR scart
20	RIN_VCR	Audio Right, Input from VCR scart
21	CVBSIN_TV	CVBS Input, from TV scart
22	LIN_TV	Audio Left, Input from TV scart
23	RIN_TV	Audio Right, Input from TV scart
24	V_{CCA}	Audio Supply Voltage - or - Audio Supply Decoupling
25	GNDA	Audio Ground
26	NC	Not Connected
27	ROUT_CINCH	Audio Right Output, to Cinch
28	LOUT_CINCH	Audio Left Output, to Cinch
29	ROUT_VCR	Audio Right Output, to VCR scart
30	LOUT_VCR	Audio Left Output, to VCR scart
31	V_{CCAO}	Audio Outputs Supply Voltage - or - Main Audio Supply Voltage
32	ROUT_TV	Audio Right Output, to TV scart
33	LOUT_TV	Audio Left Output, to TV scart
34	FILTER	Chroma Trap Filter
35	AOUT_RF	Audio(L+R) Output to RF Modulator
36	VOUT_RF	CVBS Video Output to RF Modulator
37	V_{CCB5}	Video Output Buffers Supply Pin
38	BOUT_TV	Blue Output, to TV scart
39	V_{CCB4}	Video Output Buffers Supply Pin
40	GOUT_TV	Green Output, to TV scart
41	GNDB	Video Buffers Ground
42	R/COUT_TV	Red/Chroma Output, to TV scart
43	V_{CCB3}	Video Output Buffers Supply Pin
44	Y/CVBSOUT_TV	Y/CVBS Output, to TV scart
45	V_{CCB2}	Video Output Buffers Supply Pin
46	COUT_VCR	Chroma Output, to VCR scart
47	V_{CCB1}	Video Output Buffers Supply Pin
48	Y/CVBSOUT_VCR	Y/CVBS Output, to VCR scart
49	FBOUT_TV	Fast Blanking Output, to TV scart
50	FBIN_VCR	Fast Blanking Input, from VCR scart
51	FBIN_ENC	Fast Blanking Input, from Encoder
52	C_GATE	External Mos Command for C_VCR bidirectional mode
53	V_{DD}	+5V I^2C Supply
54	ADD	I^2C Address Selection
55	SCL	I^2C Bus Clock
56	SDA	I^2C Bus Data
57	GND	Ground Digital
58	IT_OUT	Interrupt Output
59	SLB_TV	Slow Blanking Input/Output, from TV scart
60	R/CIN_VCR	Red Input (or C Input), from VCR scart
61	SLB_VCR	Slow Blanking Input/Output, from VCR scart
62	GIN_VCR	Green Input, from VCR scart
63	V_{CC12}	+12V Supply
64	BIN_VCR	Blue Input, from VCR scart

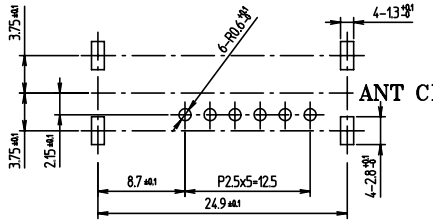
6412-01.TBL

A3

NO	PART NAME	Q'TY	MATERIAL	FINISH	REMARK

NO	NAME
1	MAIN POWER
2	VIDEO IN
3	AUDIO IN
4	CONTROL B+
5	SDA
6	SCL





Dimension of P.C.B for RF Modulator Mounting (Viewed from Copper Pattern)

Rev.	DATE	WRITTEN BY	CHECKED BY	REVISION RECORD	REMARK
3					
2					
1					

UNIT	m / m	DRAW	DESIGNED	CHECKED	APPROVED	NAME	PART NAME	OUTDRAWING
SCALE	2 / 1	CAD	H.J.H	L.N.K	K.T.H		MODEL NAME	RF MOD 14SERIES
TOLERANCE	±0.5	2002.08.06					SEMCO P/N	RMUP74055AB
SAMSUNG ELECTRO-MECHANICS		File name RMUP74055AB		3RD ANGLE PROJECTION			NO.	B-10300-14000ZZ-0

A3

A3(420X297)

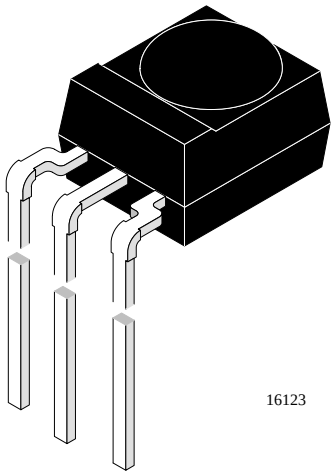
Photo Modules for PCM Remote Control Systems

Available types for different carrier frequencies

Type	fo	Type	fo
TSOP4830SB1	30 kHz	TSOP4833SB1	33 kHz
TSOP4836SB1	36 kHz	TSOP4837SB1	36.7 kHz
TSOP4838SB1	38 kHz	TSOP4840SB1	40 kHz
TSOP4856SB1	56 kHz		

Description

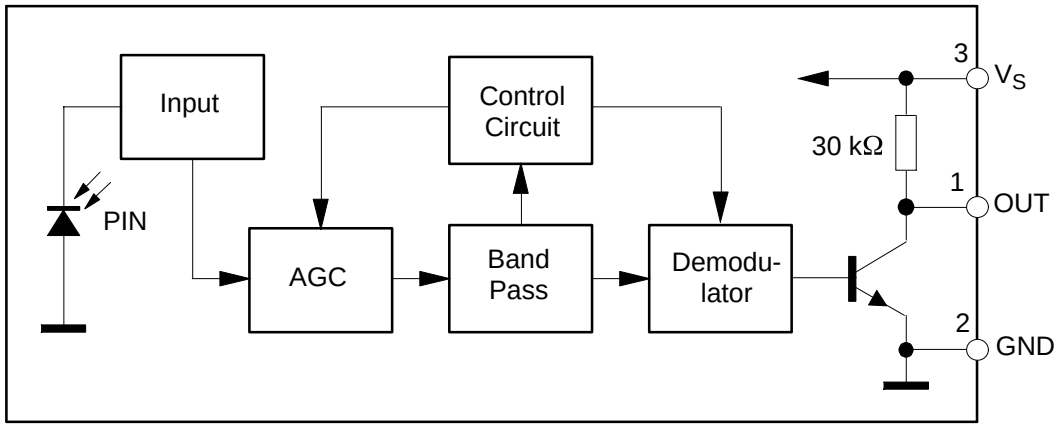
The TSOP48..SB1 – series are miniaturized receivers for infrared remote control systems. PIN diode and preamplifier are assembled on lead frame, the epoxy package is designed as IR filter.
The demodulated output signal can directly be decoded by a microprocessor. TSOP48..SB1 is the standard IR remote control receiver series, supporting all major transmission codes.



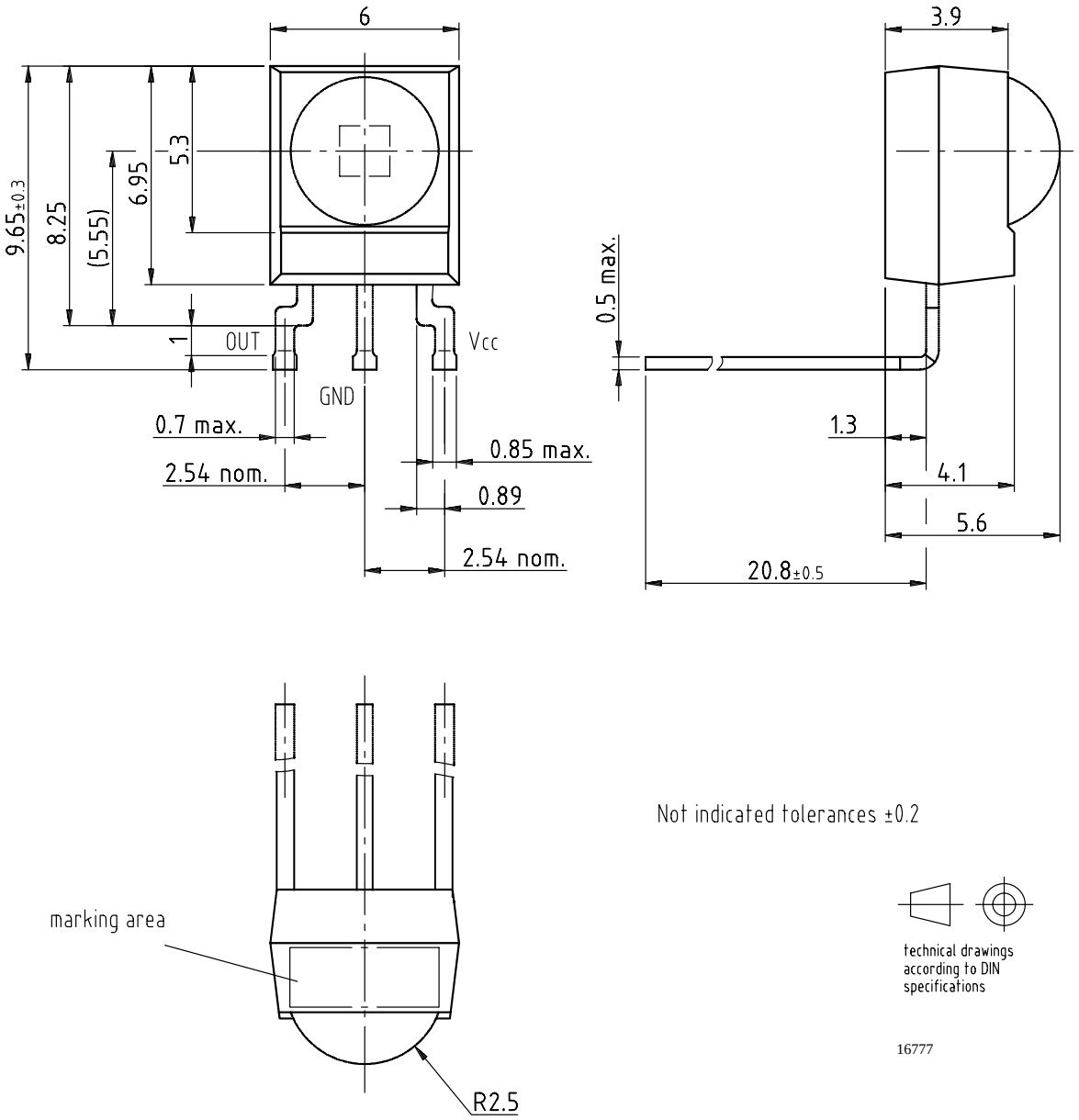
Features

- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Improved shielding against electrical field disturbance
- TTL and CMOS compatibility
- Output active low
- Low power consumption
- High immunity against ambient light
- Continuous data transmission possible (800 bit/s)
- Suitable burst length ≥10 cycles/burst

Block Diagram



Dimensions in mm



Not indicated tolerances ±0.2

technical drawings
according to DIN
specifications

16777

Data sheet acquired from Harris Semiconductor
SCHS155A

CD54/74HC164, CD54/74HCT164

High Speed CMOS Logic 8-Bit Serial-In/Parallel-Out Shift Register

October 1997 - Revised May 2000

Features

- **Buffered Inputs**
- **Asynchronous Master Reset**
- **Typical f_{MAX} = 50MHz at V_{CC} = 5V, C_L = 15pF, $T_A = 25^oC$**
- **Fanout (Over Temperature Range)**
  - **Standard Outputs . . . . . 10 LSTTL Loads**
  - **Bus Driver Outputs . . . . . 15 LSTTL Loads**
- **Wide Operating Temperature Range . . . -55^{o}C to 125^{o}C**
- **Balanced Propagation Delay and Transition Times**
- **Significant Power Reduction Compared to LSTTL Logic ICs**
- **HC Types**
  - **2V to 6V Operation**
  - **High Noise Immunity: N_{IL} = 30%, N_{IH} = 30% of V_{CC} at V_{CC} = 5V**
- **HCT Types**
  - **4.5V to 5.5V Operation**
  - **Direct LSTTL Input Logic Compatibility, V_{IL}= 0.8V (Max), V_{IH} = 2V (Min)**
  - **CMOS Input Compatibility, $I_I \leq 1\mu A$ at V_{OL}, V_{OH}**

Description

The ’HC164 and ’HCT164 are 8-bit serial-in parallel-out shift registers with asynchronous reset. Data is shifted on the positive edge of Clock (CP). A LOW on the Master Reset ($\overline{MR}$) pin resets the shift register and all outputs go to the LOW state regardless of the input conditions. Two Serial Data inputs (DS1 and DS2) are provided, either one can be used as a Data Enable control.

Ordering Information

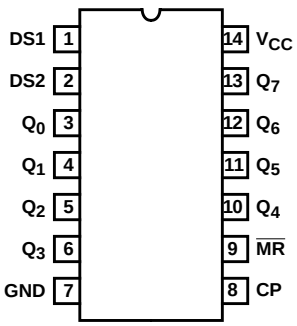
PART NUMBER	TEMP. RANGE (^{o}C)	PACKAGE
CD54HC164F	-55 to 125	14 Ld CERDIP
CD54HC164F3A	-55 to 125	14 Ld CERDIP
CD74HC164E	-55 to 125	14 Ld PDIP
CD74HC164M	-55 to 125	14 Ld SOIC
CD54HCT164F3A	-55 to 125	14 Ld CERDIP
CD74HCT164E	-55 to 125	14 Ld PDIP
CD74HCT164M	-55 to 125	14 Ld SOIC

NOTE:

1. When ordering, use the entire part number. Add the suffix 96 to obtain the variant in the tape and reel.
2. Wafer and die is available which meets all electrical specifications. Please contact your local TI sales office or customer service for ordering information.

Pinout

CD54HC164, CD54HCT164
(CERDIP)
CD74HC164, CD74HCT164
(PDIP, SOIC)
TOP VIEW



KA431/KA431A/KA431L

Programmable Shunt Regulator

Features

- Programmable output voltage to 36 volts
- Low dynamic output impedance 0.20 typical
- Sink current capability of 1.0 to 100mA
- Equivalent full-range temperature coefficient of 50ppm/ °C typical
- Temperature compensated for operation over full rated operating temperature range
- Low output noise voltage
- Fast turn-on response

Description

The KA431/KA431A/KA431L are three-terminal adjustable regulator series with a guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between VREF (approximately 2.5 volts) and 36 volts with two external resistors These devices have a typical dynamic output impedance of 0.2W Active output circuitry provides a very sharp turn on characteristic,
making these devices excellent replacement for zener diodes in many applications.

TO-92

1. Ref 2. Anode 3. Cathode

8-DIP

1.Cathode 2.3.4.5.7.NC
6.Anode 8.Ref

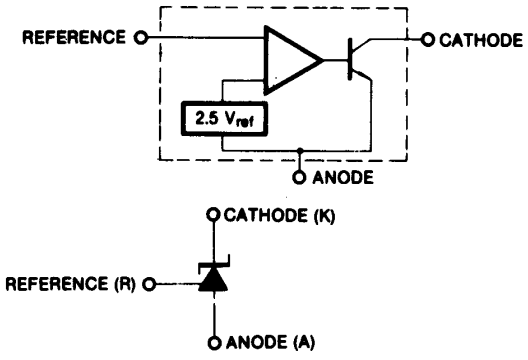
8-SOP

1.Cathode 2.3.6.7.Anode
4.5.NC 8.Ref

SOT-23F

1. Cathode 2. Ref 3. Anode

Internal Block Diagram



Rev. 1.0.4

KA1L0380B/KA1L0380RB/ KA1M0380RB/KA1H0380RB

Fairchild Power Switch(FPS)

Features

- Precision fixed operating frequency
- KA1L0380B/KA1L0380RB (50KHz)
- KA1M0380RB (67KHz)
- KA1H0380RB (100KHz)
- Pulse by pulse over current limiting
- Over load protection
- Over voltage protection (Min. 23V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto restart (KA1L0380RB/KA1M0380RB/KA1H0380RB)

Description

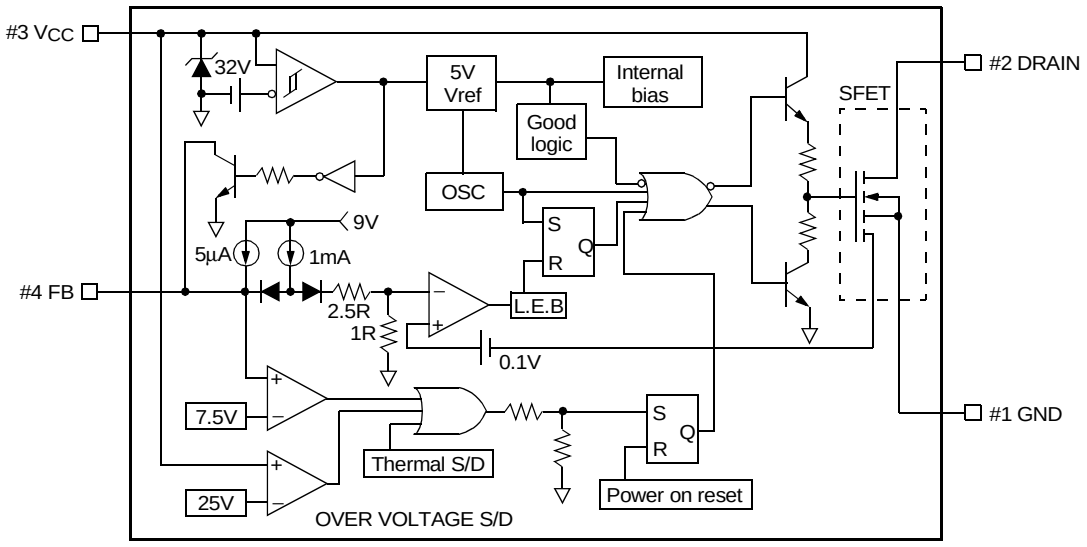
The Fairchild Power Switch(FPS) product family is specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM controller IC. PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/ turn-off driver, thermal shut down protection, over voltage protection, temperature compensated precision current sources for loop compensation and fault protection circuit. compared to discrete MOSFET and controller or R_{CC} switching converter solution, a Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase & efficiency, productivity, and system reliability. It has a basic platform well suited for cost effective design in either a flyback converter or a forward converter.

TO-220F-4L

1

1. GND 2. DRAIN 3. V_{CC} 4. FB

Internal Block Diagram



Rev.1.0.2

KA278R33

Low Dropout Voltage Regulator

Features

- 2A / 3.3V Output low dropout voltage regulator
- TO220 Full-Mold package (4PIN)
- Overcurrent protection, Thermal shutdown
- Overvoltage protection, Short-Circuit protection
- With output disable function

Description

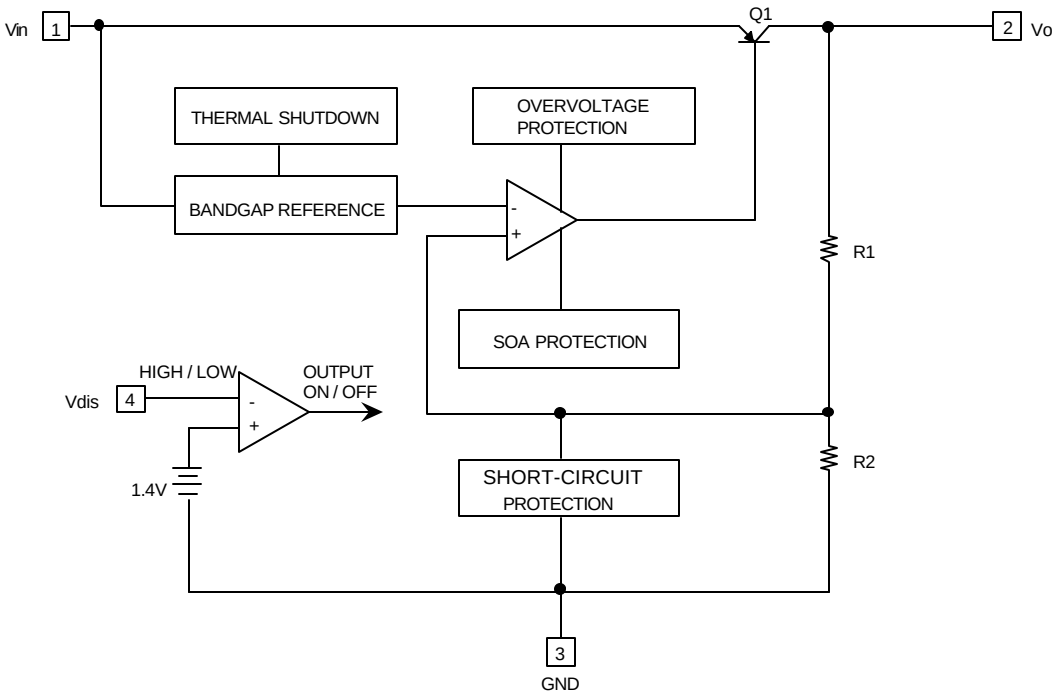
The KA278R33 is a low-dropout voltage regulator suitable for various electronic equipments. It provides constant voltage power source with TO-220 4 lead full mold package. Dropout voltage of KA278R33 is below 0.5V in full rated current(2A). This regulator has various function such as peak current protection, thermal shut down, overvoltage protection and output disable function.

TO-220F-4L

1

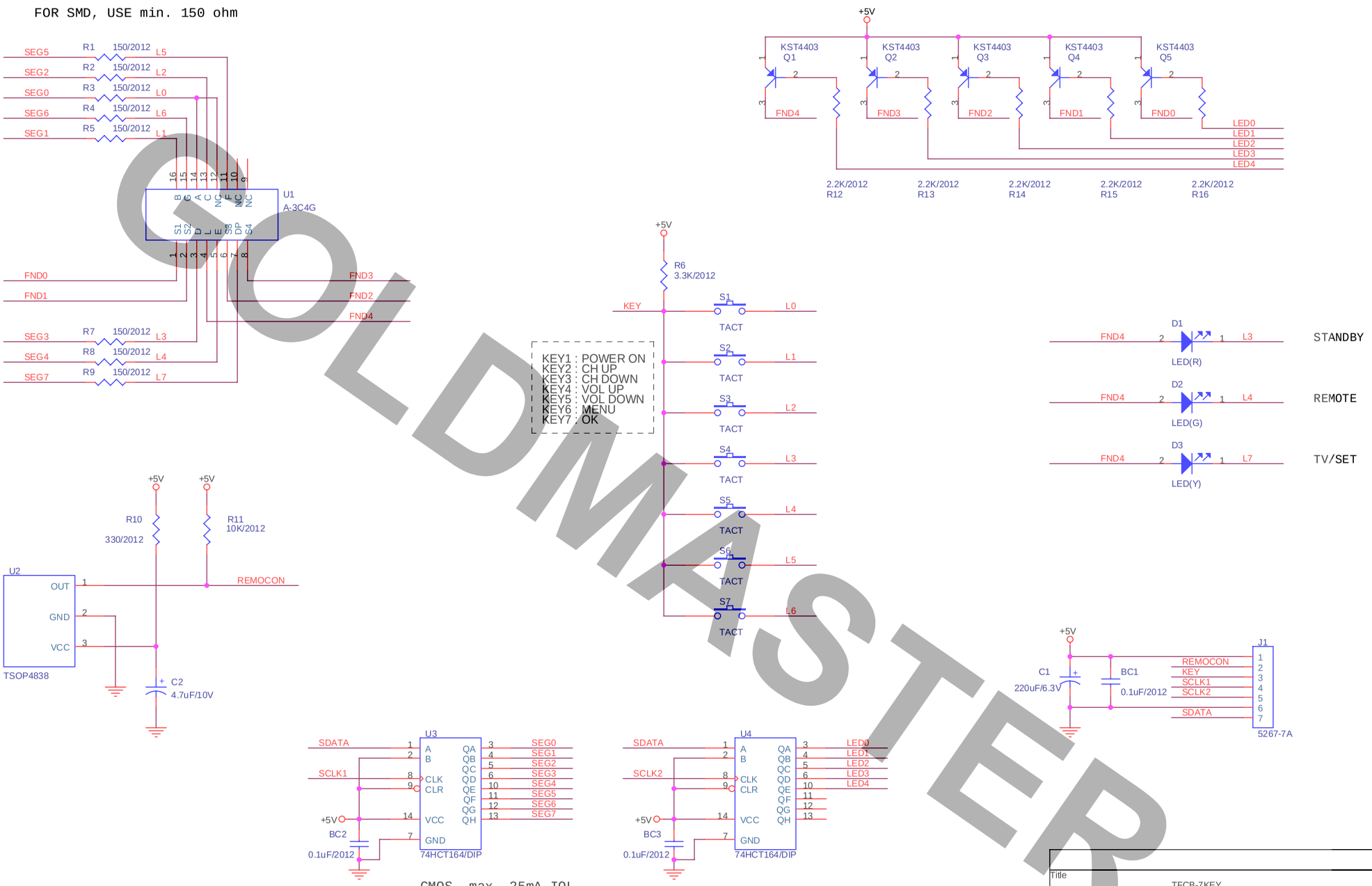
1. Vin 2. Vo 3. GND 4. Vdis

Internal Block Diagram

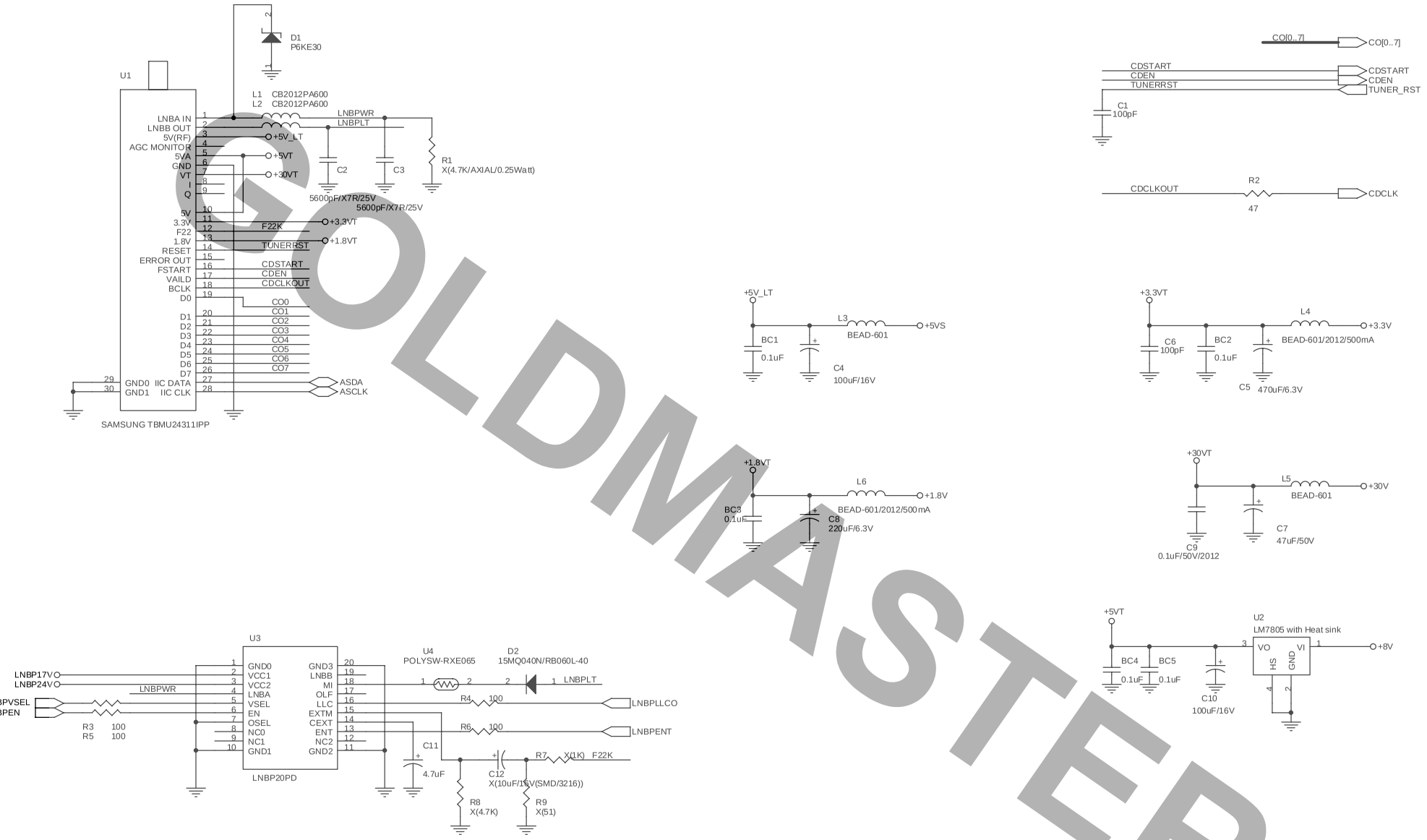


Rev.1.0.1

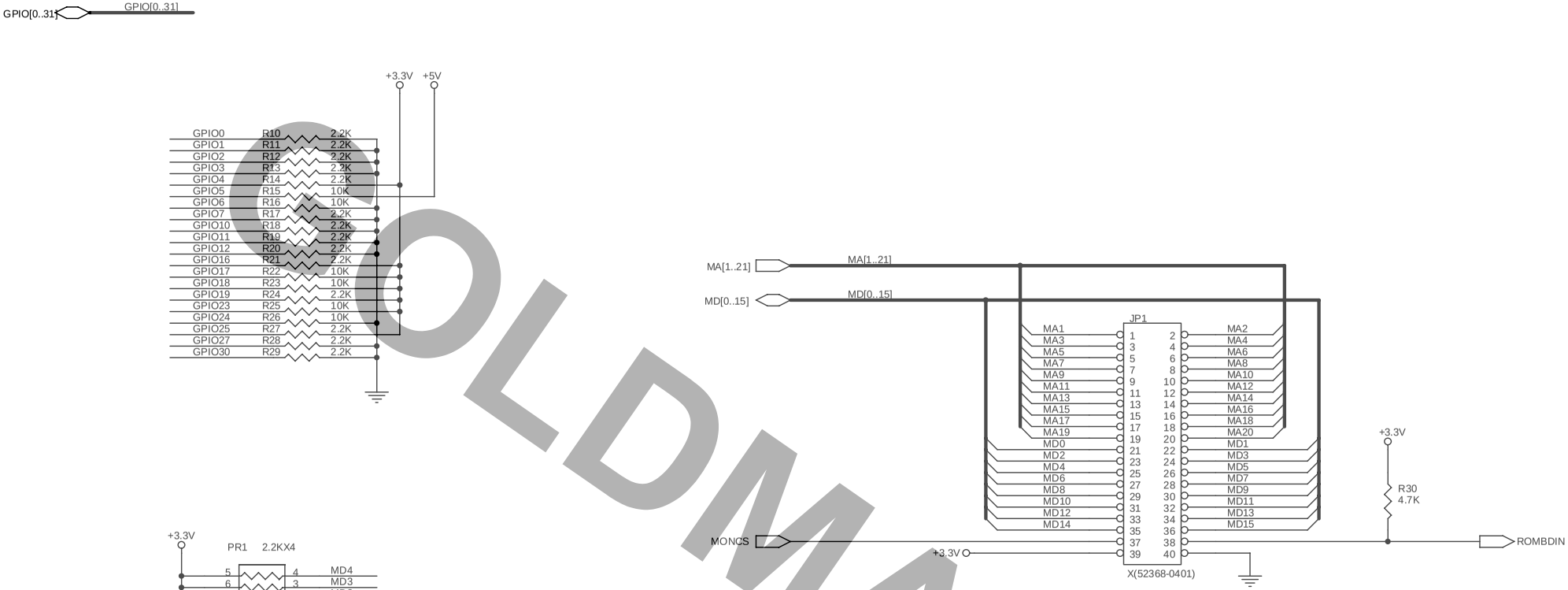
A. 2 Schematic Diagram Section < Front Board >


FOR SMD, USE min. 150 ohm
SEG5
SEG2
SEG0
SEG6
SEG1
R1 150/2012
R2 150/2012
R3 150/2012
R4 150/2012
R5 150/2012
L5
L2
L0
L6
L1
U1
A-3C4G
B C A C NC NC F NC NC
S1 S2 D L E S3 DP S4
FND0
FND1
FND3
FND2
FND4
SEG3
SEG4
SEG7
R7 150/2012
R8 150/2012
R9 150/2012
L3
L4
L7
+5V
KST4403 Q1
KST4403 Q2
KST4403 Q3
KST4403 Q4
KST4403 Q5
FND4
FND3
FND2
FND1
FND0
LED0
LED1
LED2
LED3
LED4
2.2K/2012 R12
2.2K/2012 R13
2.2K/2012 R14
2.2K/2012 R15
2.2K/2012 R16
R6 3.3K/2012
KEY
S1 TACT L0
S2 TACT L1
S3 TACT L2
S4 TACT L3
S5 TACT L4
S6 TACT L5
S7 TACT L6
KEY1 : POWER ON
KEY2 : CH UP
KEY3 : CH DOWN
KEY4 : VOL UP
KEY5 : VOL DOWN
KEY6 : MENU
KEY7 : OK
D1 LED(R) L3 STANDBY
D2 LED(G) L4 REMOTE
D3 LED(Y) L7 TV/SET
R10 330/2012
R11 10K/2012
U2
OUT
GND
VCC
REMOCON
TSOP4838
C2 4.7uF/10V
C1 220uF/6.3V
BC1 0.1uF/2012
J1
REMOCON
KEY
SCLK1
SCLK2
SDATA
5267-7A
U3
SDATA
SCLK1
BC2 0.1uF/2012
A B CLK CLR VCC GND
QA QB QC QD QE QF QG QH
SEG0 SEG1 SEG2 SEG3 SEG4 SEG5 SEG6 SEG7
74HCT164/DIP
U4
SDATA
SCLK2
BC3 0.1uF/2012
LED0 LED1 LED2 LED3 LED4
74HCT164/DIP
CMOS, max. 25mA IOL
GOLDMASTER
Title TFCB-7KEY
Size B
Document Number FRONT
Rev 1.0
Date: Friday, December 05, 2003
Sheet 1 of 1

A. 3 Schematic Diagram Section < Main Board >



Title	TF5000CI	
Size	Document Number: TUNER	Rev: 1.4
Date: 토요일, 1월 31, 2004	Sheet 1 of 17	



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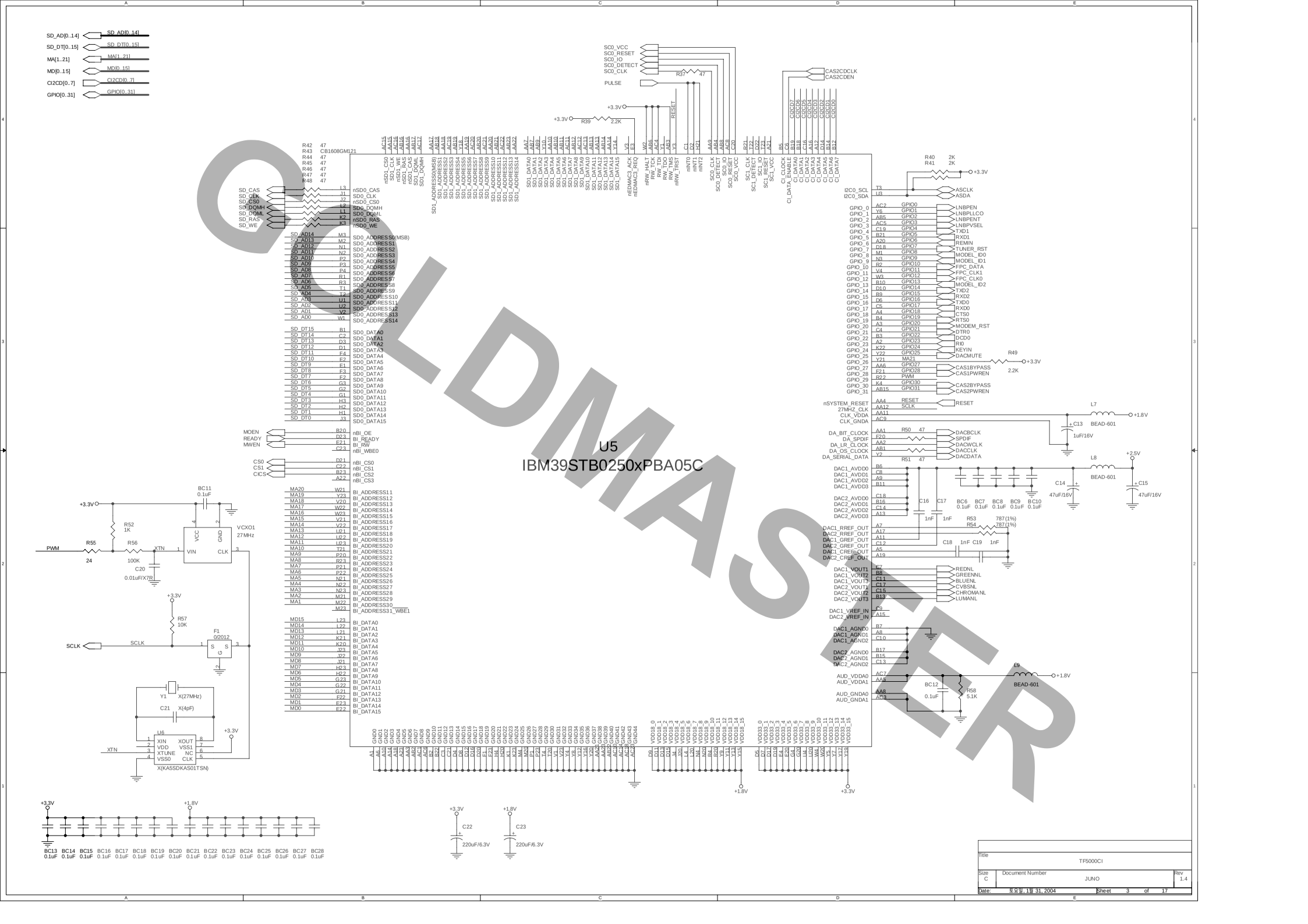
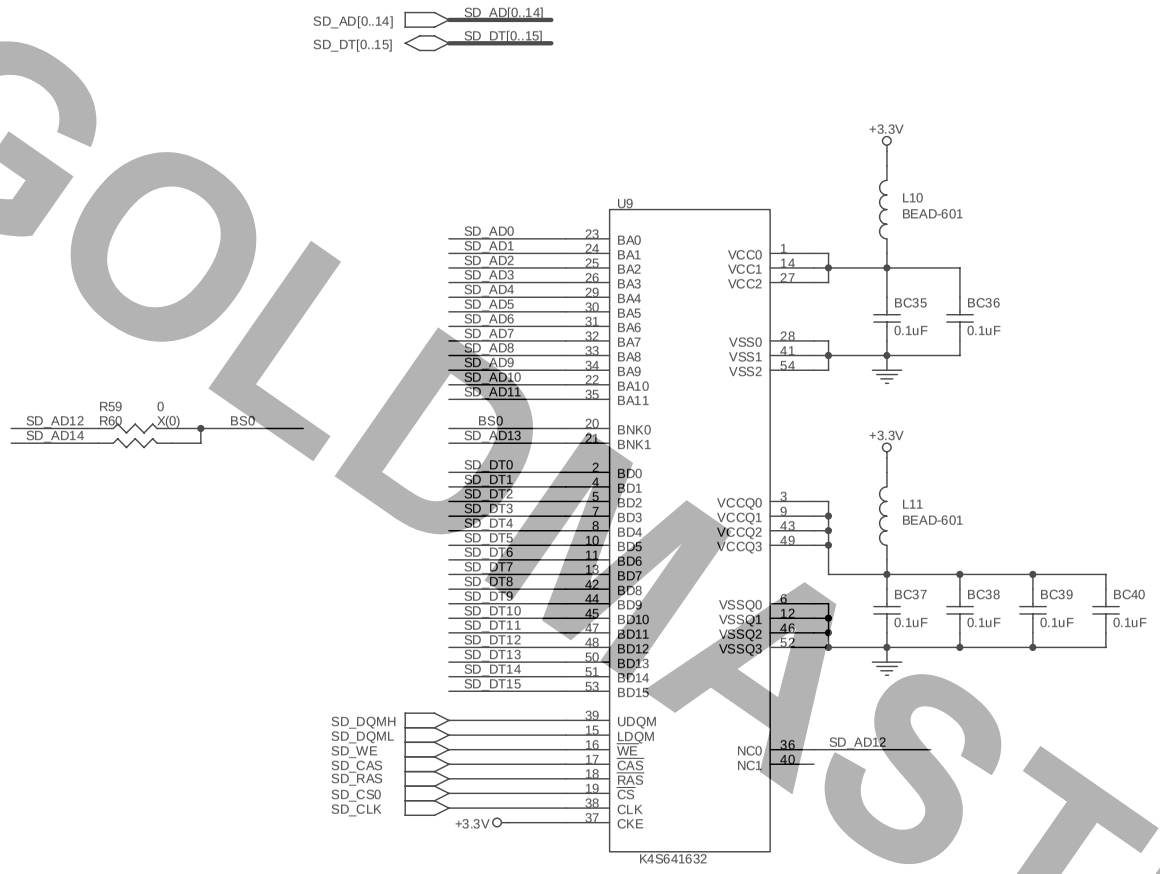
Size B	Document Number TEST	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 2 of 17	


SD_AD[0..14]
SD_DT[0..15]
MA[1..21]
MD[0..15]
CI2CD[0..7]
GPIO[0..31]
U5
IBM39STB0250xPBA05C
SD_CAS
SD_CLK
SD_CS0
SD_DQMH
SD_DQML
SD_RAS
SD_WE
MOEN
MWEN
READY
CS0
CS1
CICS
PWM
SCLK
VCXO1
27MHz
X(27MHz)
X(KA5SDKAS01TSN)
CAS2CDCLK
CAS2CDEN
PULSE
RESET
I2C0_SCL
I2C0_SDA
ASCLK
ASDA
CAS1BYPASS
CAS1PWREN
CAS2BYPASS
CAS2PWREN
DACBCLK
SPDIF
DACWCLK
DACCLK
DACDATA
REDNL
GREENNL
BLUENL
CVBSNL
CHROMANL
LUMANL
LNBPEN
LNBPLLCO
LNBPENT
LNBPVSEL
TXD1
RXD1
REMIN
TUNER_RST
MODEL_ID0
MODEL_ID1
FPC_DATA
FPC_CLK1
FPC_CLK0
MODEL_ID2
TXD2
RXD2
TXD0
RXD0
CTS0
RTS0
MODEM_RST
DTR0
DCD0
RI0
KEYIN
DACMUTE
BEAD-601
+1.8V
+2.5V
+3.3V
TF5000CI
JUNO
Rev 1.4
Sheet 3 of 17

Title	TF5000CI	
Size B	Document Number EEPROM	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 4 of 17	



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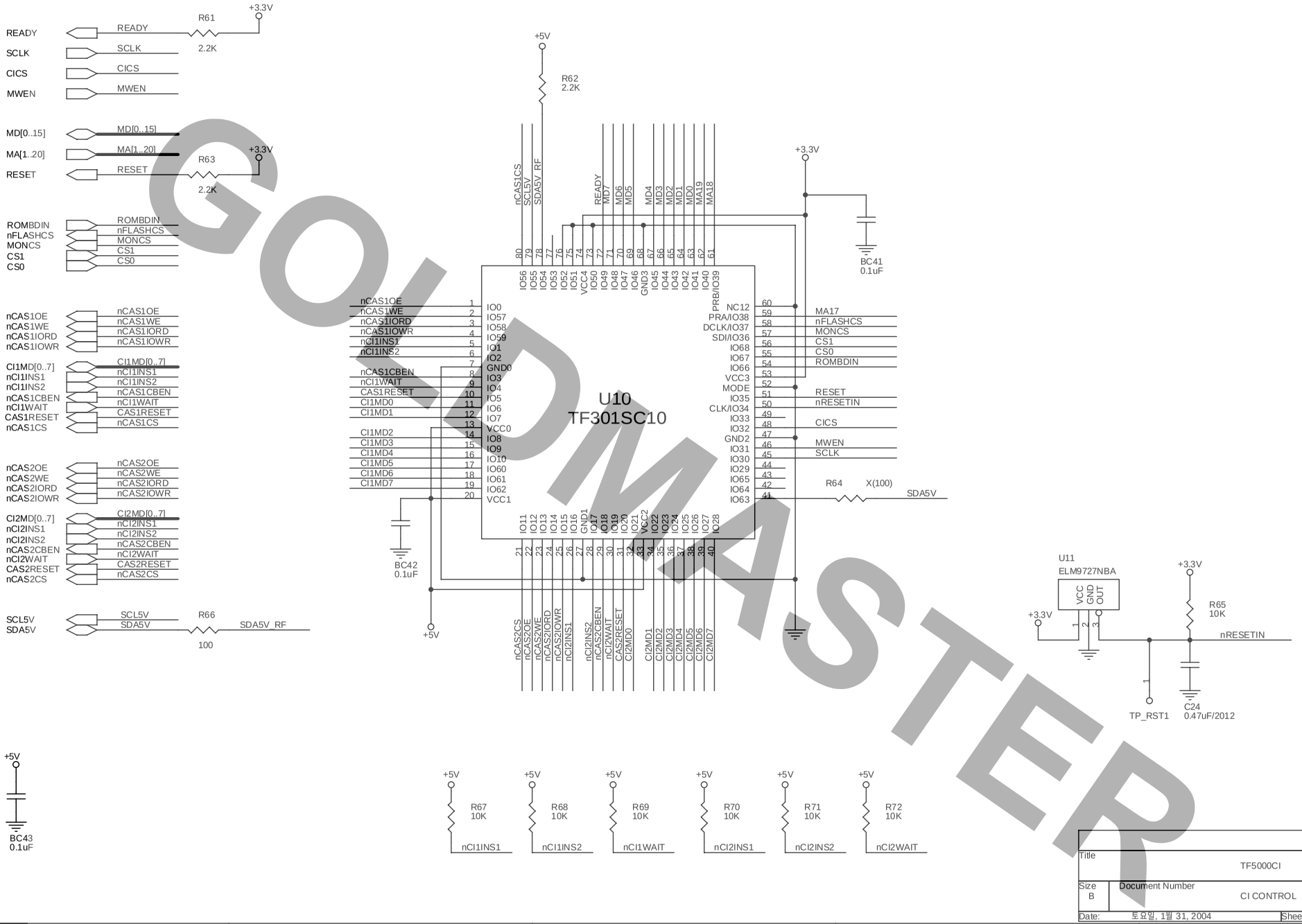
Size B	Document Number SDRAM	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 5 of 17	



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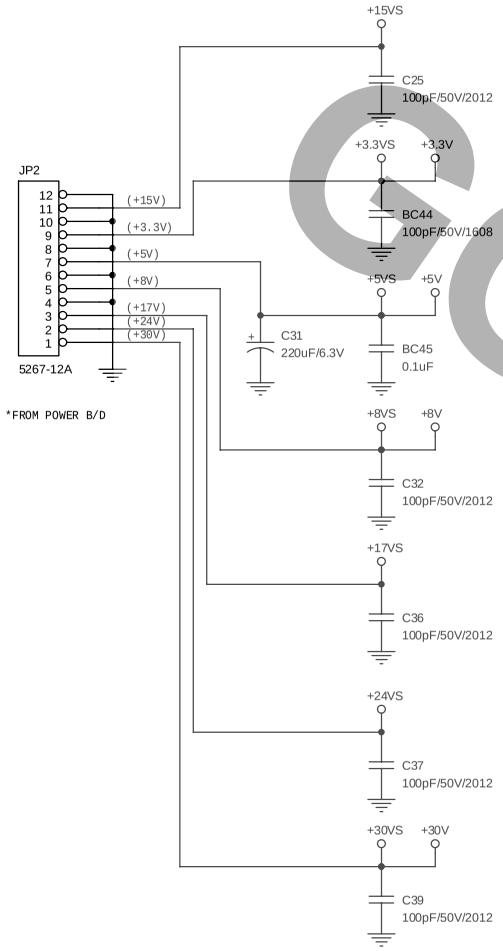
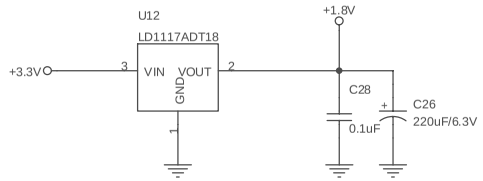
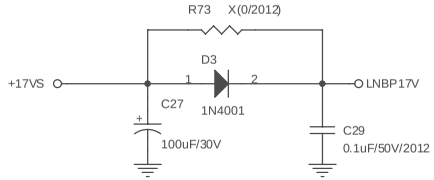
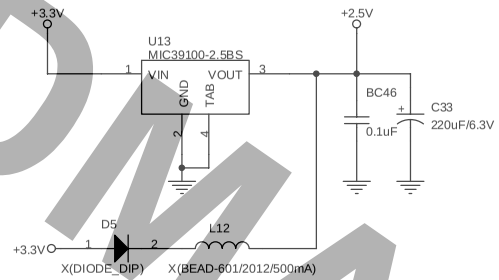
Size B	Document Number CI CONTROL	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 6 of 17	

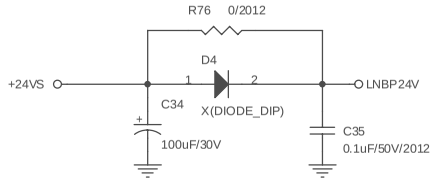


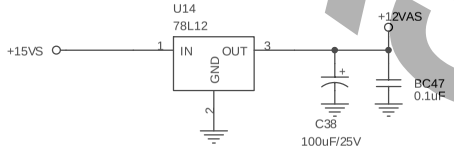
MODIFIED FROM V1.3











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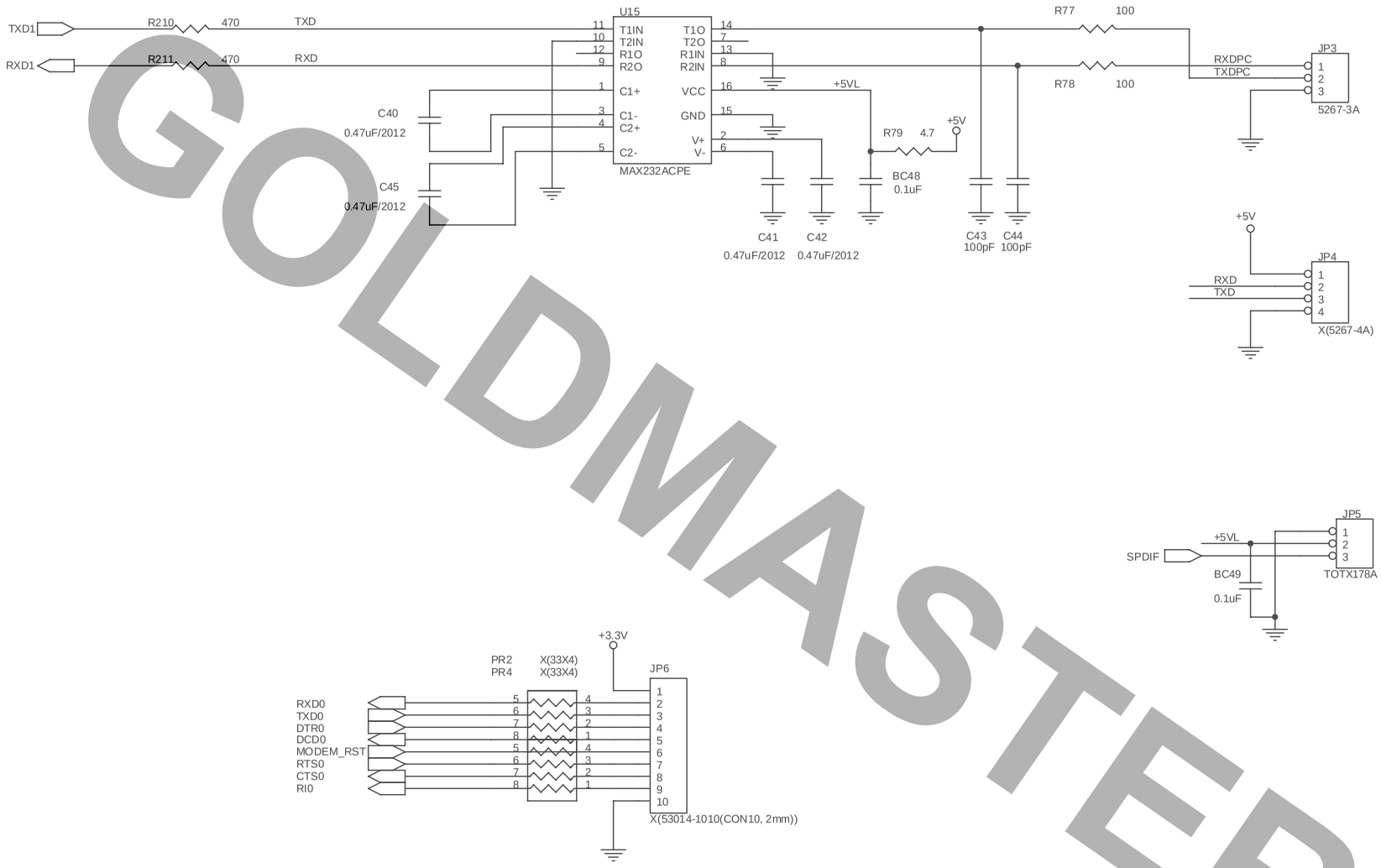
Date: 토요일, 1월 31, 2004	Sheet 7 of 17	



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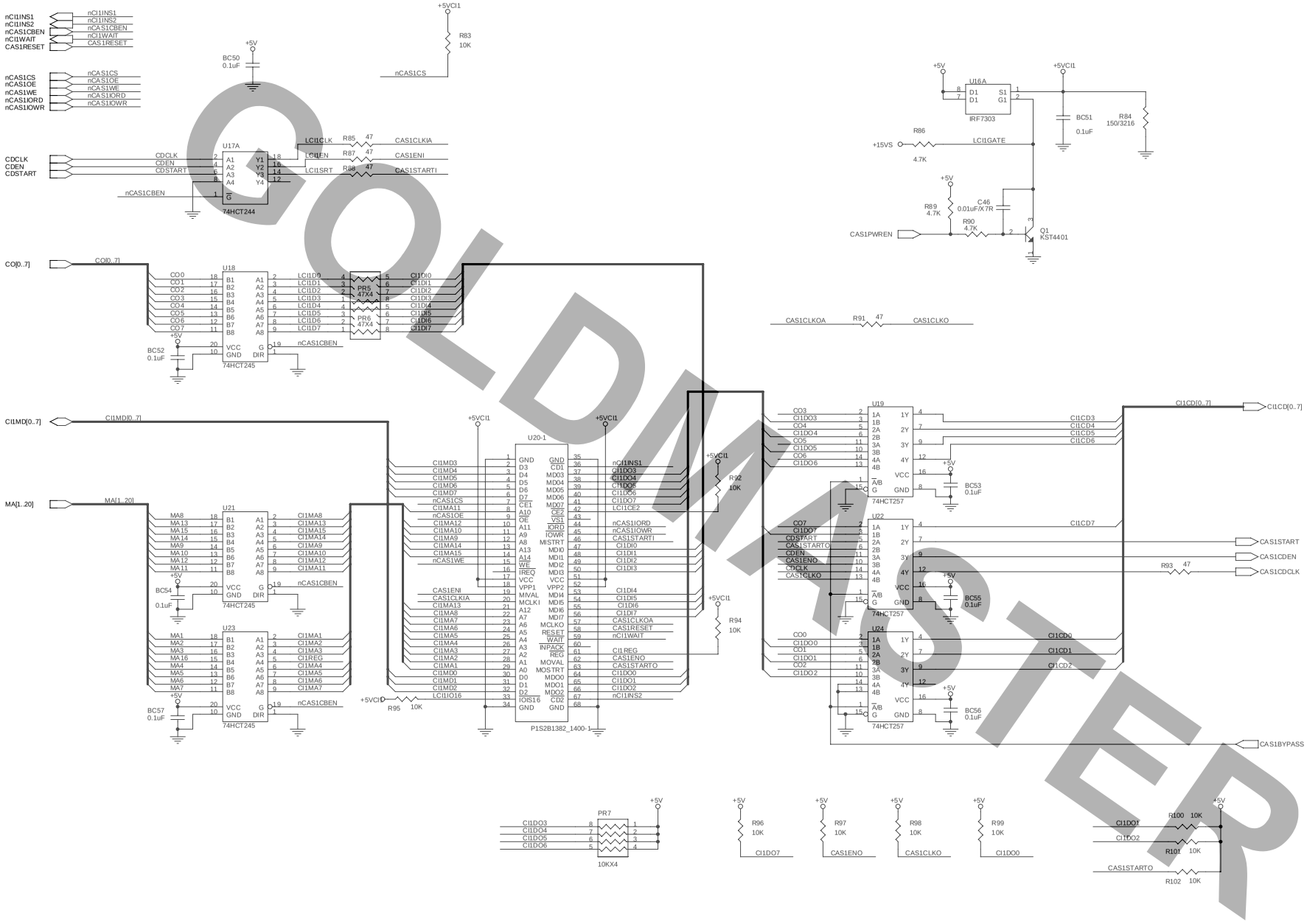
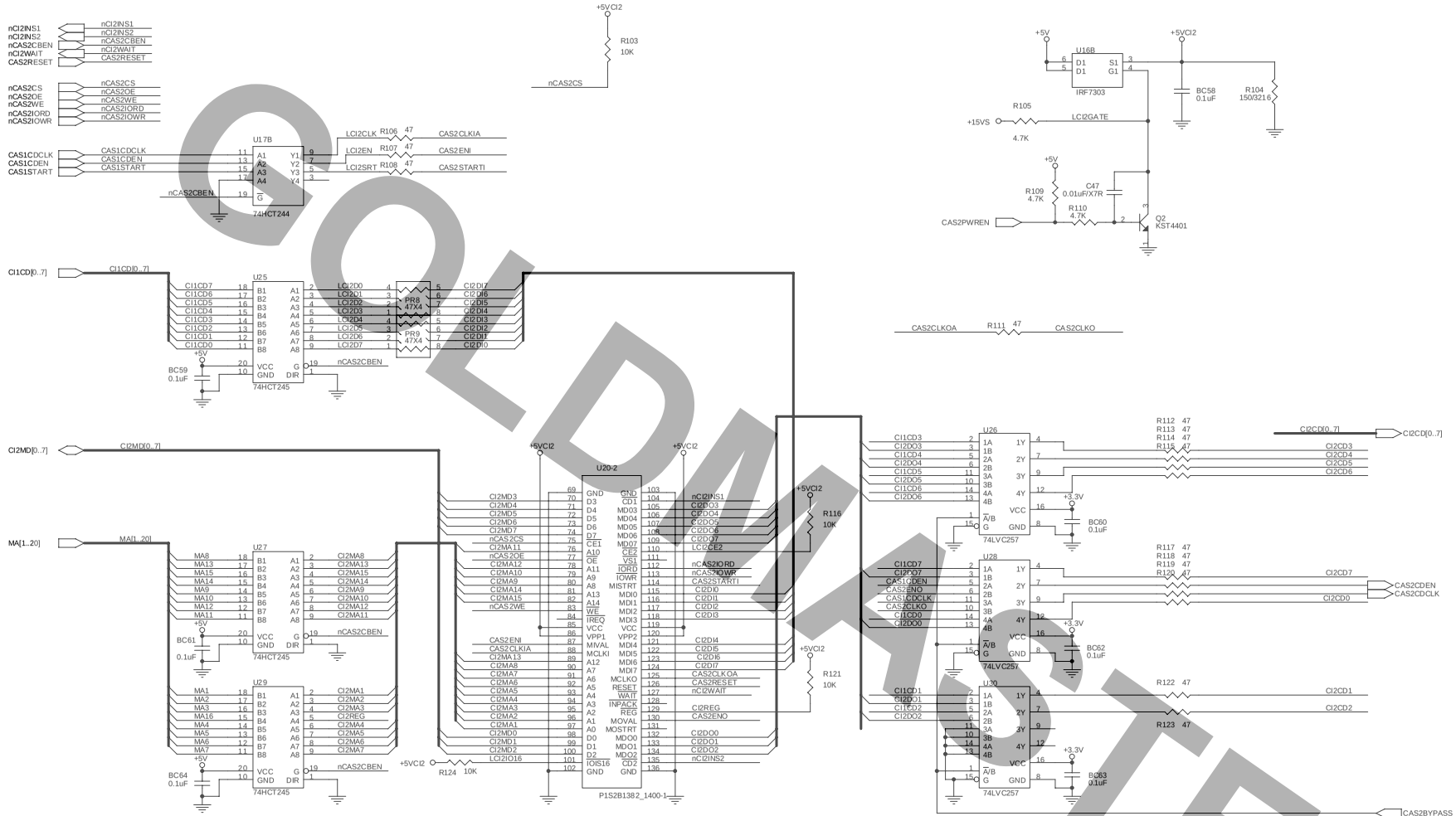
Size	Document Number: RS232	Rev: 1.4
Date: 토요일, 1월 31, 2004	Sheet 8 of 17	



Title	TF5000CI	
Size	Document Number: CI1	Rev: 1.4
Date: 토요일, 1월 31, 2004	Sheet 9 of 17	



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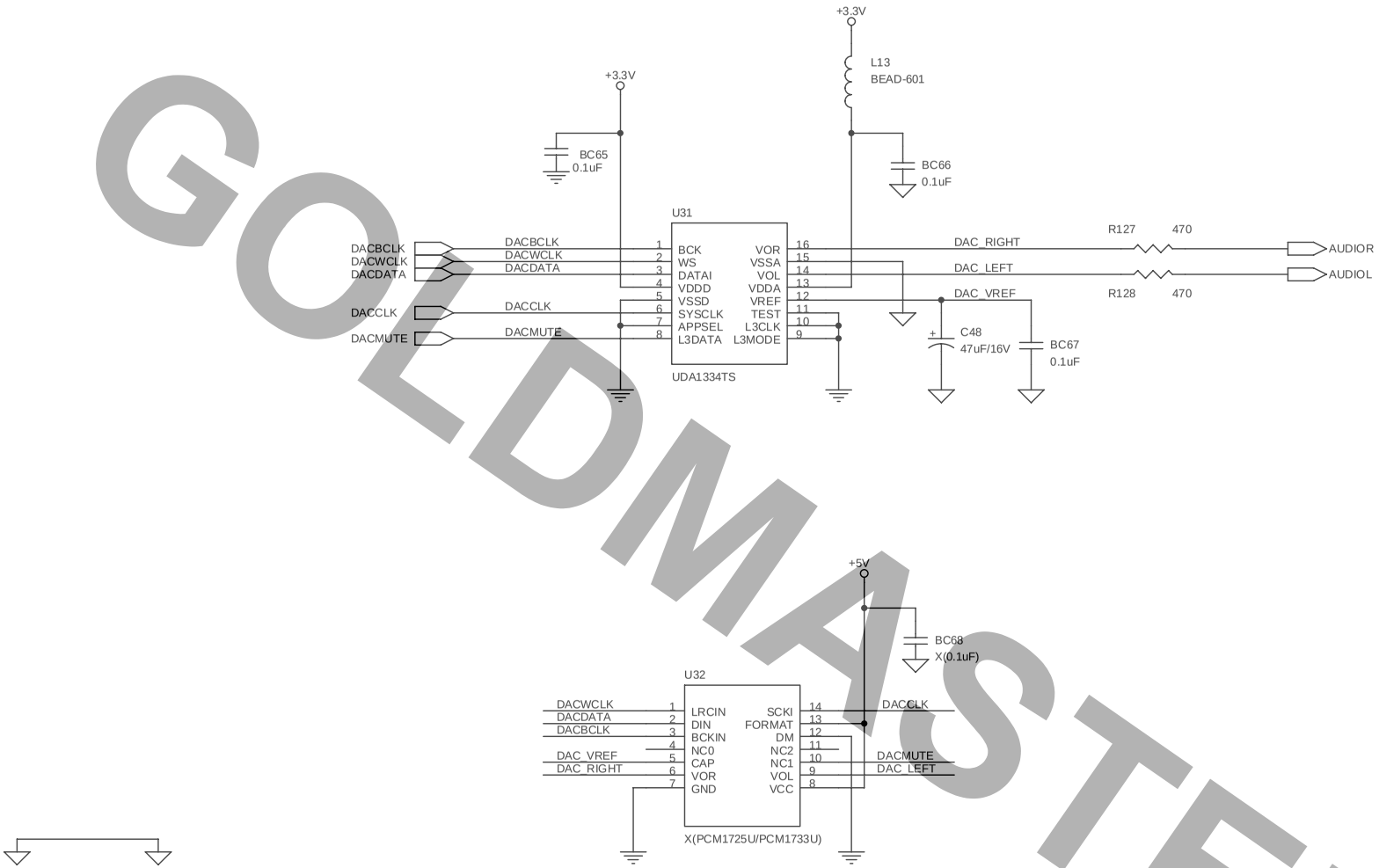
Date: 토요일, 1월 31, 2004	Sheet 10 of 17	



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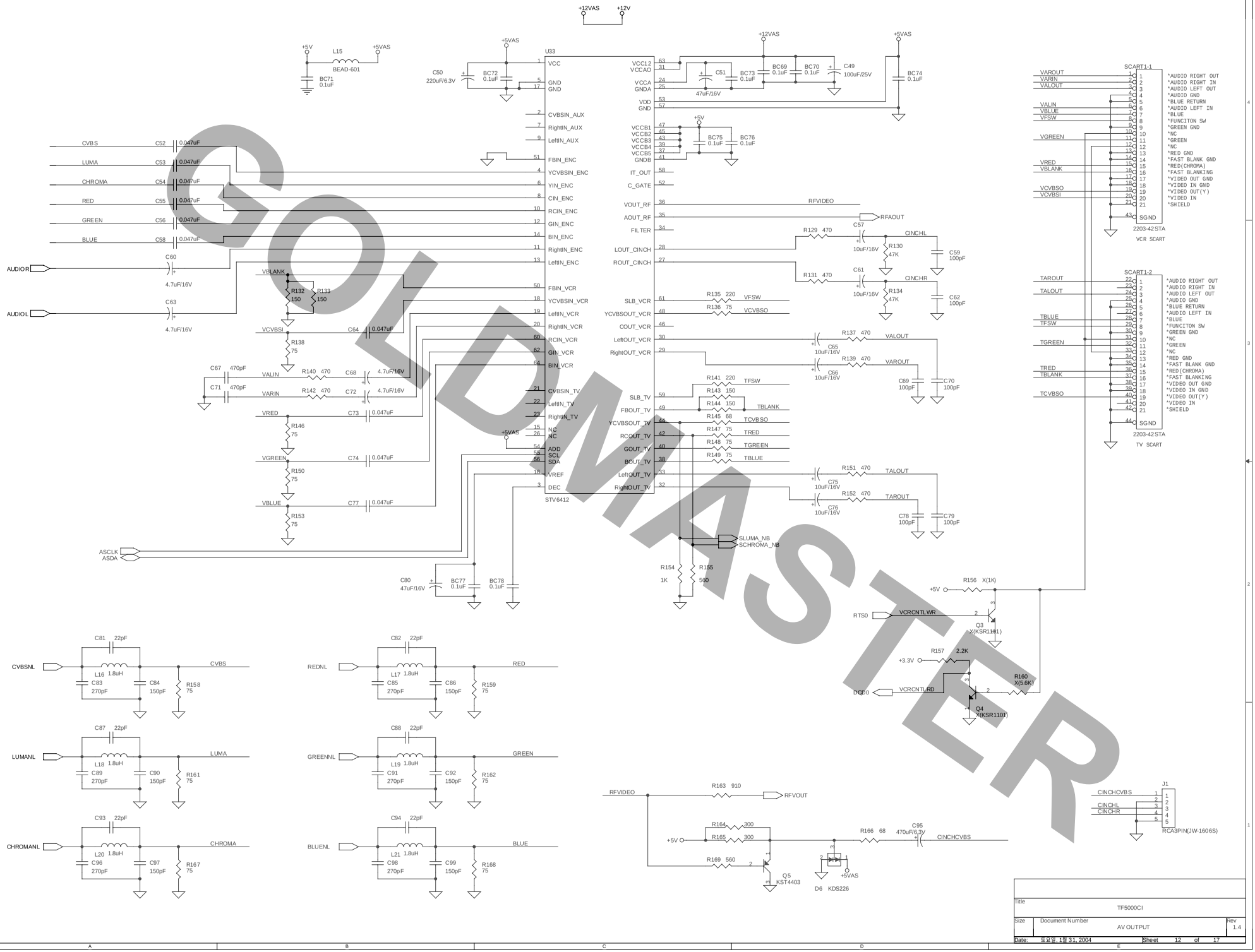
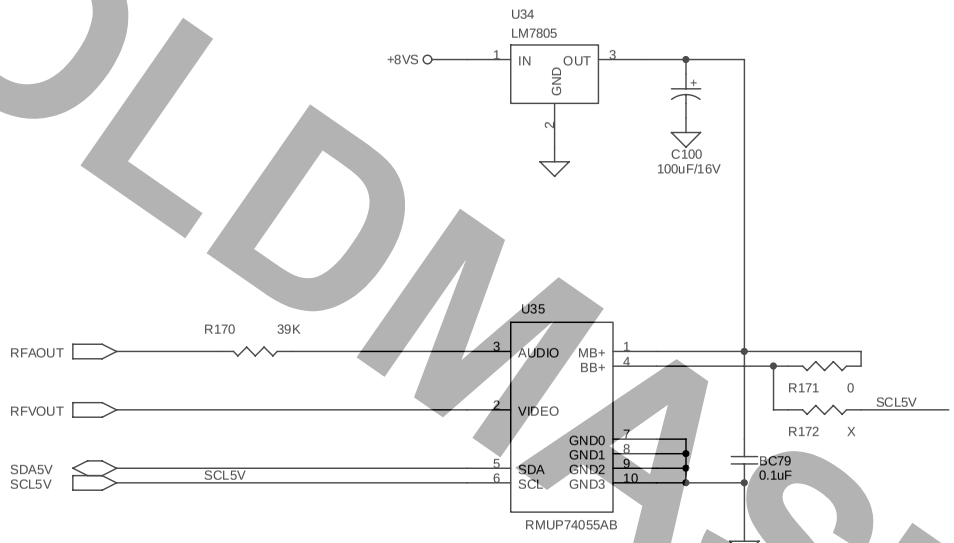
Size	Document Number: AUDIO DAC	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 11 of 17	


+12VAS
+12V
+5V
L15
BEAD-601
BC71 0.1uF
+5VAS
C50 220uF/6.3V
BC72 0.1uF
U33
VCC
GND
GND
CVBSIN_AUX
RightIN_AUX
LeftIN_AUX
FBIN_ENC
YCVBSIN_ENC
YIN_ENC
CIN_ENC
RCIN_ENC
GIN_ENC
BIN_ENC
RightIN_ENC
LeftIN_ENC
FBIN_VCR
YCVBSIN_VCR
LeftIN_VCR
RightIN_VCR
RCIN_VCR
GIN_VCR
BIN_VCR
CVBSIN_TV
LeftIN_TV
RightIN_TV
NC
NC
ADD
SCL
SDA
VREF
DEC
STV6412
VCC12
VCCAO
VCCA
GNDA
VDD
GND
VCCB1
VCCB2
VCCB3
VCCB4
VCCB5
GNDB
IT_OUT
C_GATE
VOUT_RF
AOUT_RF
FILTER
LOUT_CINCH
ROUT_CINCH
SLB_VCR
YCVBSOUT_VCR
COUT_VCR
LeftOUT_VCR
RightOUT_VCR
SLB_TV
FBOUT_TV
YCVBSOUT_TV
RCOUT_TV
GOUT_TV
BOUT_TV
LeftOUT_TV
RightOUT_TV
C51 47uF/16V
BC73 0.1uF
BC69 0.1uF
BC70 0.1uF
C49 100uF/25V
BC74 0.1uF
BC75 0.1uF
BC76 0.1uF
RFVIDEO
RFAOUT
C57 10uF/16V
R129 470
CINCHL
R130 47K
C59 100pF
R131 470
C61 10uF/16V
CINCHR
R134 47K
C62 100pF
R135 220
VFSW
R136 75
VCVBSO
R137 470
VALOUT
C65 10uF/16V
R139 470
VAROUT
C66 10uF/16V
C69 100pF
C70 100pF
R141 220
TFSW
R143 150
R144 150
TBLANK
R145 68
TCVBSO
R147 75
TRED
R148 75
TGREEN
R149 75
TBLUE
R151 470
TALOUT
C75 10uF/16V
R152 470
TAROUT
C76 10uF/16V
C78 100pF
C79 100pF
SLUMA_NB
SCHROMA_NB
R154 1K
R155 560
C80 47uF/16V
BC77 0.1uF
BC78 0.1uF
CVBS
LUMA
CHROMA
RED
GREEN
BLUE
C52 0.047uF
C53 0.047uF
C54 0.047uF
C55 0.047uF
C56 0.047uF
C58 0.047uF
AUDIOR
AUDIOL
C60 4.7uF/16V
C63 4.7uF/16V
VBLANK
R132 150
R133 150
VCVBSI
C64 0.047uF
R138 75
C67 470pF
C71 470pF
VALIN
VARIN
R140 470
R142 470
C68 4.7uF/16V
C72 4.7uF/16V
VRED
C73 0.047uF
R146 75
VGREEN
C74 0.047uF
R150 75
VBLUE
C77 0.047uF
R153 75
ASCLK
ASDA
SCART1-1
VAROUT
VARIN
VALOUT
VALIN
VBLUE
VFSW
VGREEN
VRED
VBLANK
VCVBSO
VCVBSI
*AUDIO RIGHT OUT
*AUDIO RIGHT IN
*AUDIO LEFT OUT
*AUDIO GND
*BLUE RETURN
*AUDIO LEFT IN
*BLUE
*FUNCITON SW
*GREEN GND
*NC
*GREEN
*NC
*RED GND
*FAST BLANK GND
*RED(CHROMA)
*FAST BLANKING
*VIDEO OUT GND
*VIDEO IN GND
*VIDEO OUT(Y)
*VIDEO IN
*SHIELD
SGND
2203-42STA
VCR SCART
SCART1-2
TAROUT
TALOUT
TBLUE
TFSW
TGREEN
TRED
TBLANK
TCVBSO
*AUDIO RIGHT OUT
*AUDIO RIGHT IN
*AUDIO LEFT OUT
*AUDIO GND
*BLUE RETURN
*AUDIO LEFT IN
*BLUE
*FUNCITON SW
*GREEN GND
*NC
*GREEN
*NC
*RED GND
*FAST BLANK GND
*RED(CHROMA)
*FAST BLANKING
*VIDEO OUT GND
*VIDEO IN GND
*VIDEO OUT(Y)
*VIDEO IN
*SHIELD
SGND
2203-42STA
TV SCART
R156 X(1K)
RTS0
VCRCNTLWR
Q3 X(KSR1101)
R157 2.2K
+3.3V
DCD0
VCRCNTLRD
R160 X(5.6K)
Q4 X(KSR1101)
CVBSNL
REDNL
GREENL
BLUENL
LUMANL
CHROMANL
C81 22pF
C82 22pF
C87 22pF
C88 22pF
C93 22pF
C94 22pF
L16 1.8uH
L17 1.8uH
L18 1.8uH
L19 1.8uH
L20 1.8uH
L21 1.8uH
C83 270pF
C84 150pF
C85 270pF
C86 150pF
C89 270pF
C90 150pF
C91 270pF
C92 150pF
C96 270pF
C97 150pF
C98 270pF
C99 150pF
R158 75
R159 75
R161 75
R162 75
R167 75
R168 75
RFVIDEO
R163 910
RFVOUT
R164 300
R165 300
R169 560
Q5 KST4403
R166 68
C95 470uF/6.3V
CINCHCVBS
D6 KDS226
+5VAS
J1
CINCHCVBS
CINCHL
CINCHR
RCA3PIN(JW-1606S)
Title
TF5000CI
Size
Document Number
AV OUTPUT
Rev
1.4
Date: 토요일, 1월 31, 2004
Sheet 12 of 17



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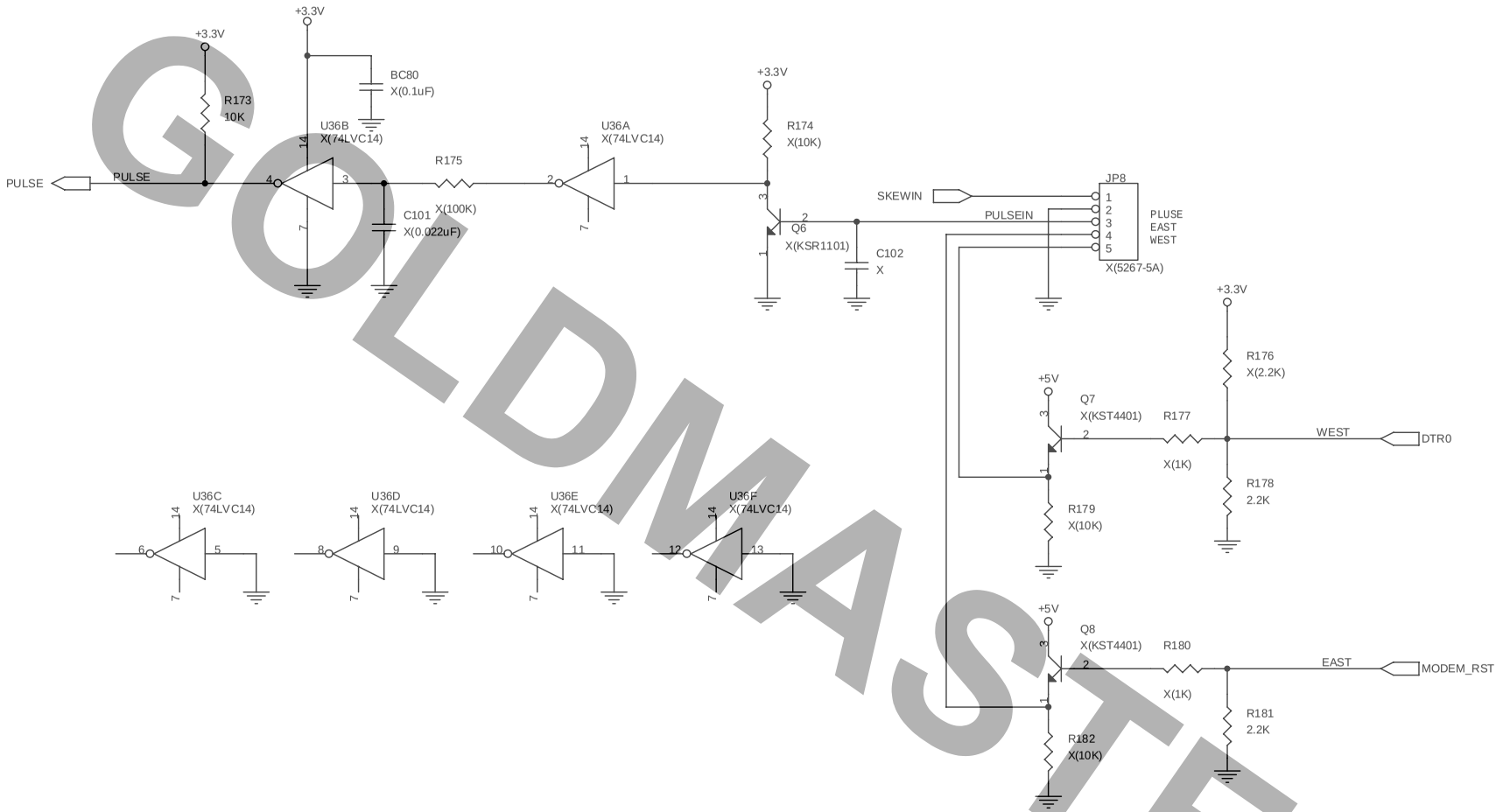
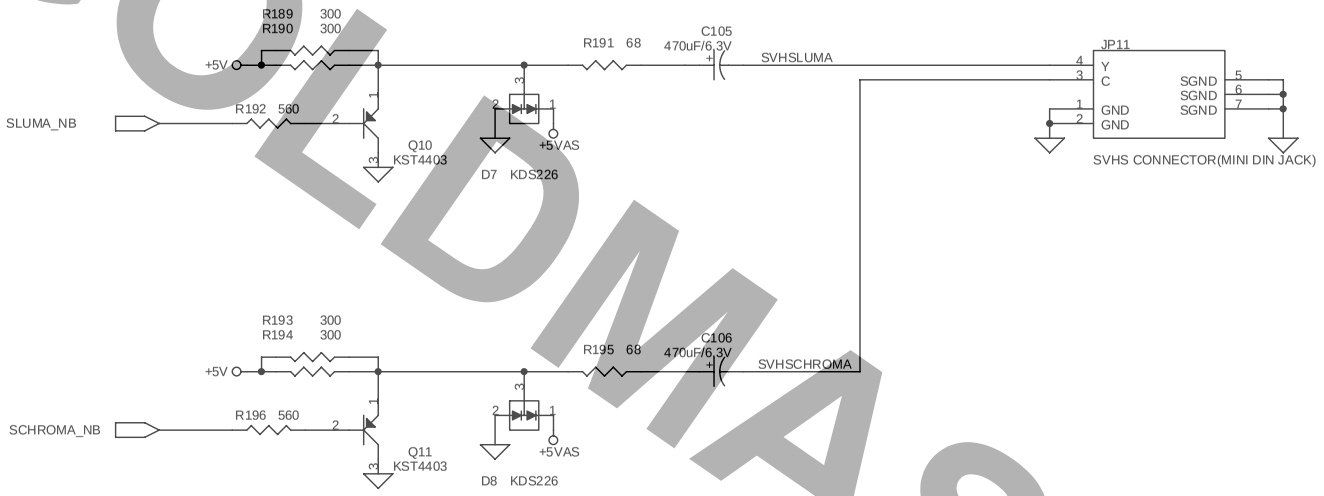
Size B	Document Number RF MODULATOR	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 13 of 17	

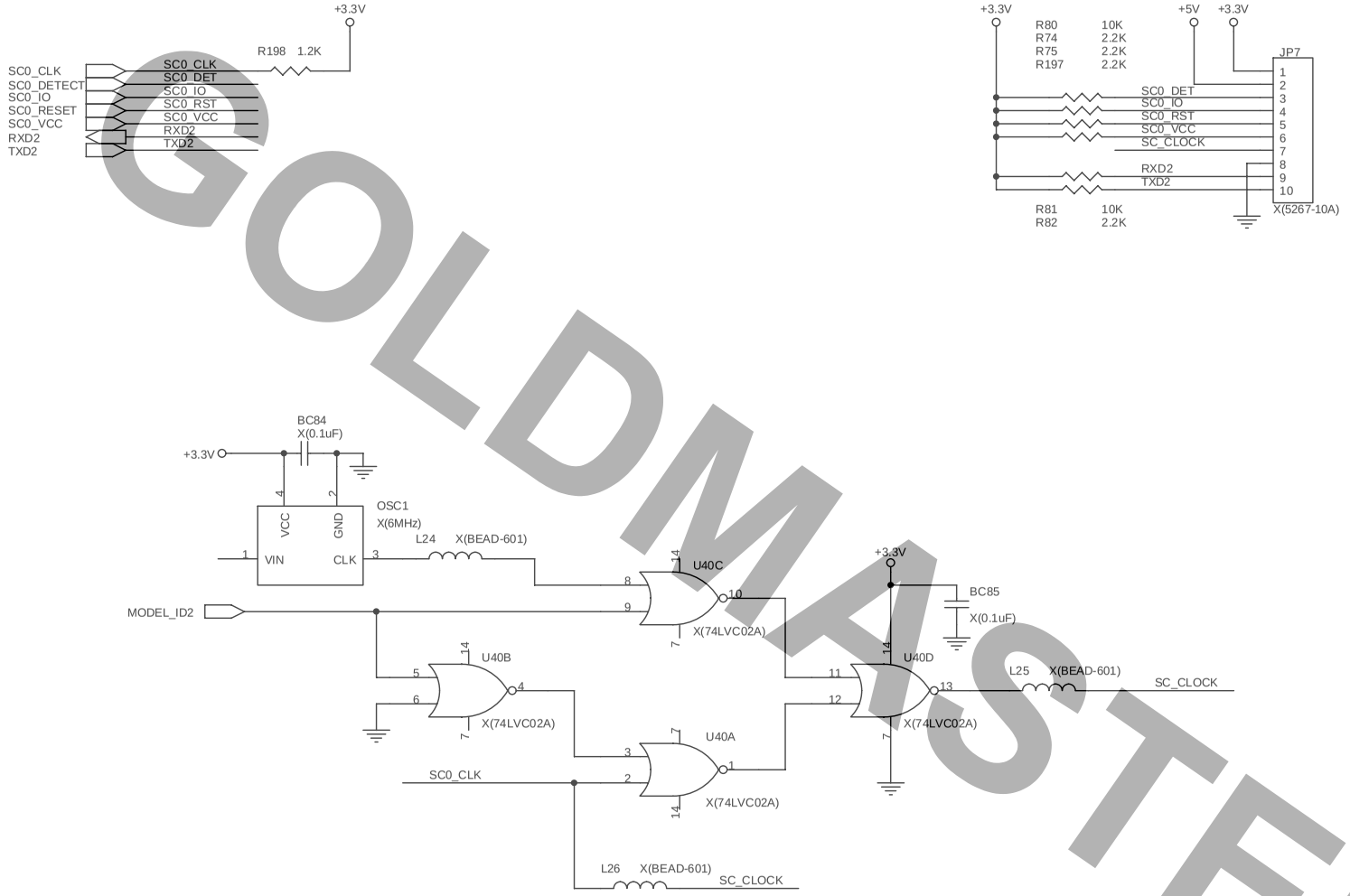


Title	TF5000CI	
Size B	Document Number POSITIONER	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 14 of 17	

Title	TF5000CI	
Size B	Document Number FRONT	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 15 of 17	

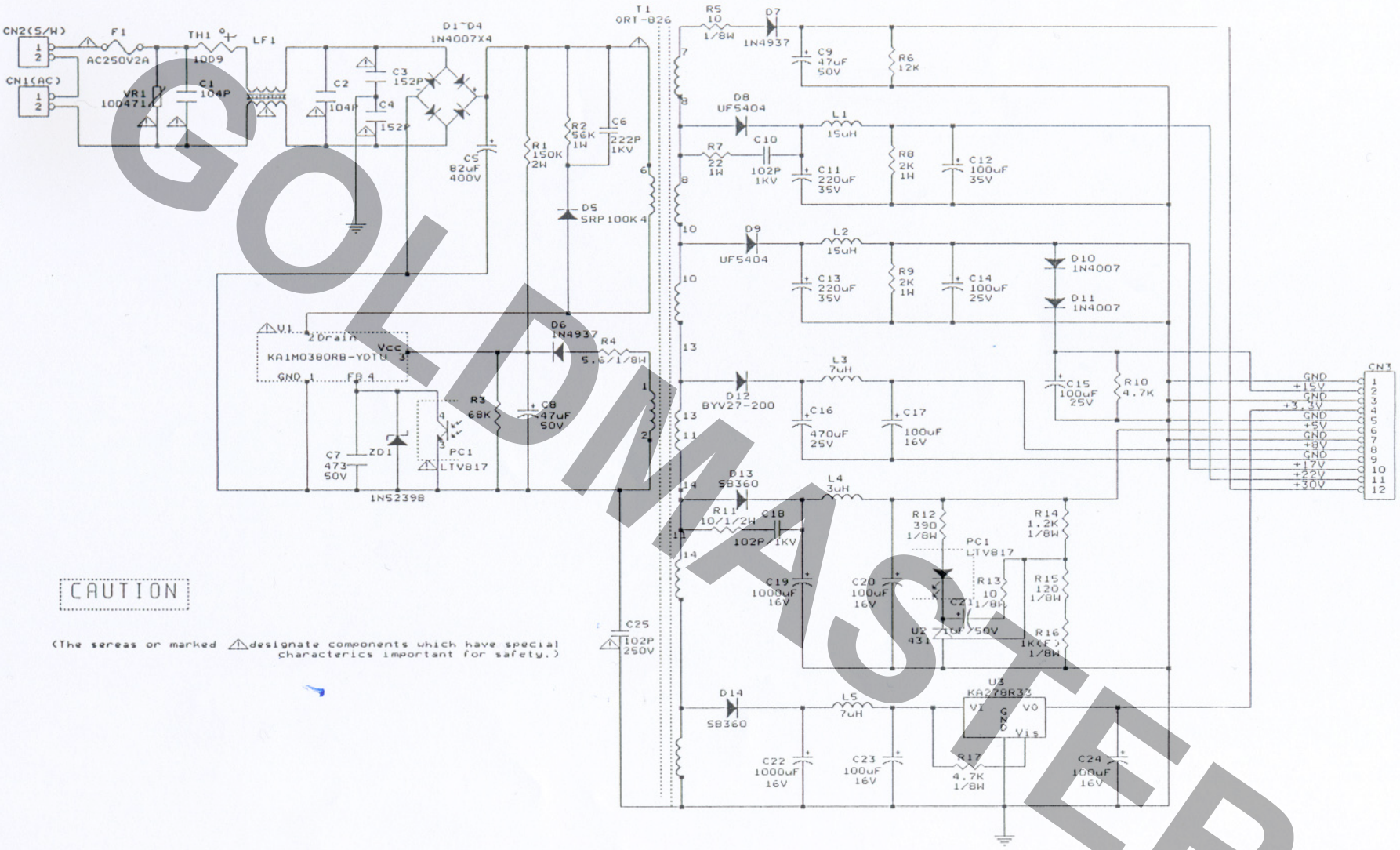


Title	TF5000CI	
Size B	Document Number SVHS	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 16 of 17	



Title	TF5000CI	
Size B	Document Number SMART CARD	Rev 1.4
Date: 토요일, 1월 31, 2004	Sheet 17 of 17	

A. 4 Schematic Diagram Section < SMPS >



CAUTION

(The sereas or marked ⚠ designate components which have special characterics important for safety.)

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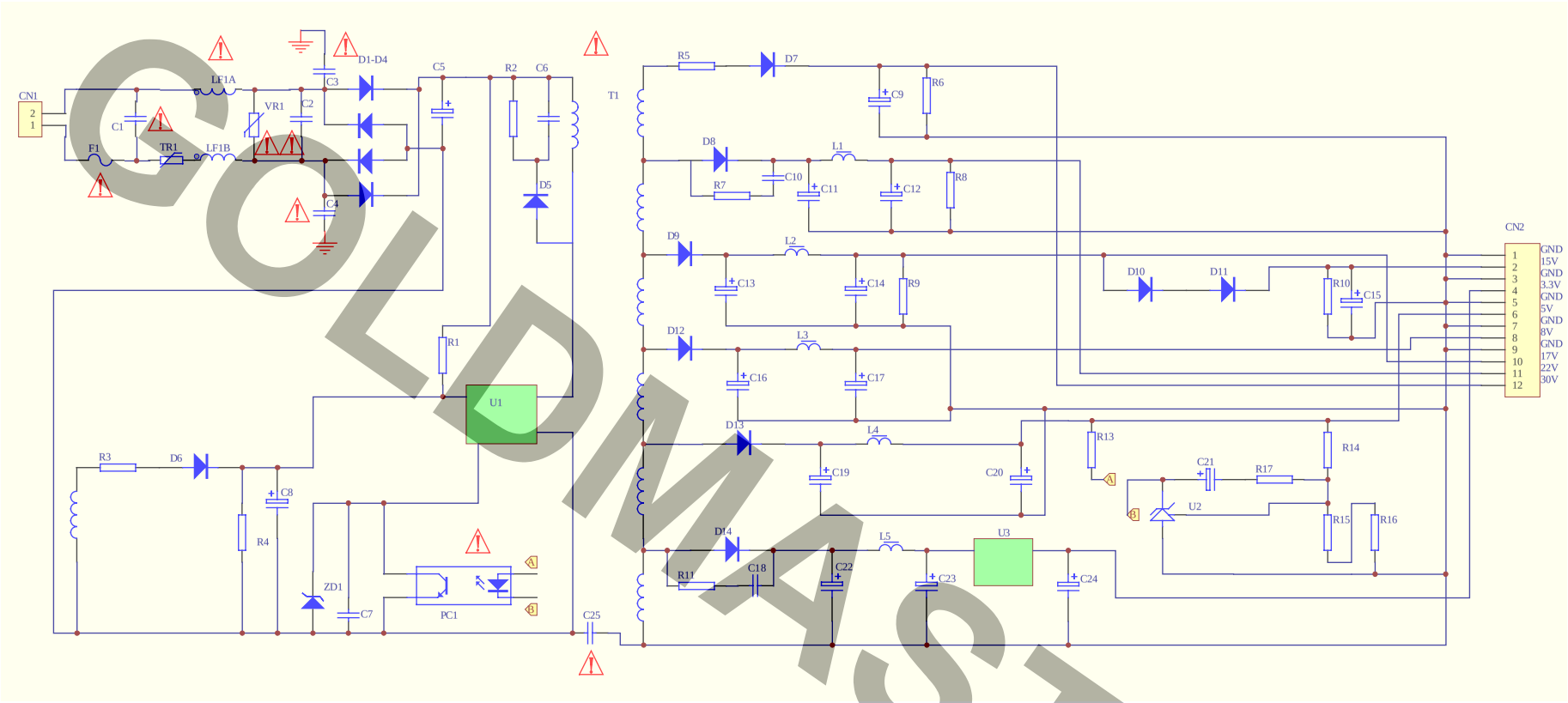
Date: November 21, 2001	Sheet of	



				Wuxi Hard Electronics Co.,Ltd				Approved	Check	Drawing
				Model	HDAD30W701	Unit	mm			
				Part no		Scale				
				Rev.	01					
Rev.	Date	Description	Check	Page	1 OF 1					